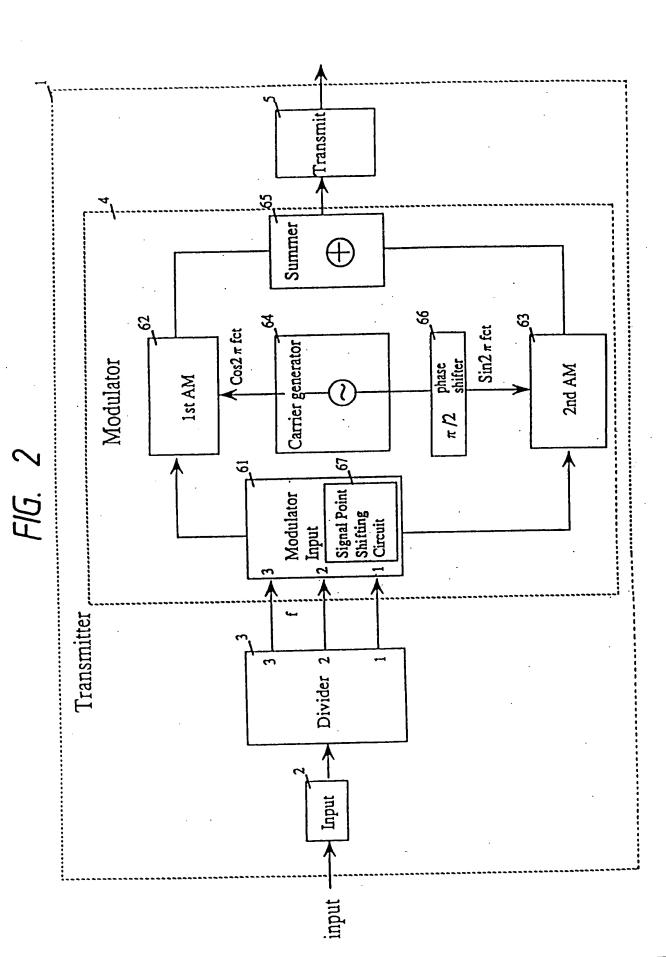
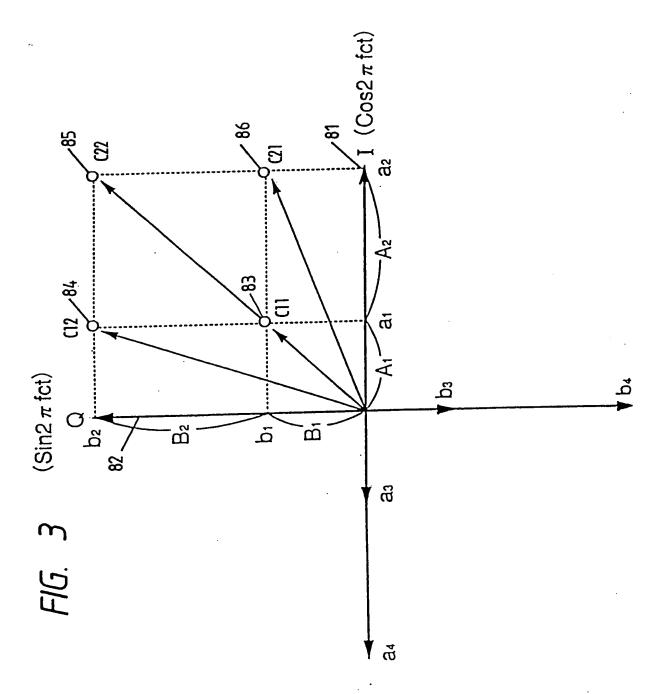
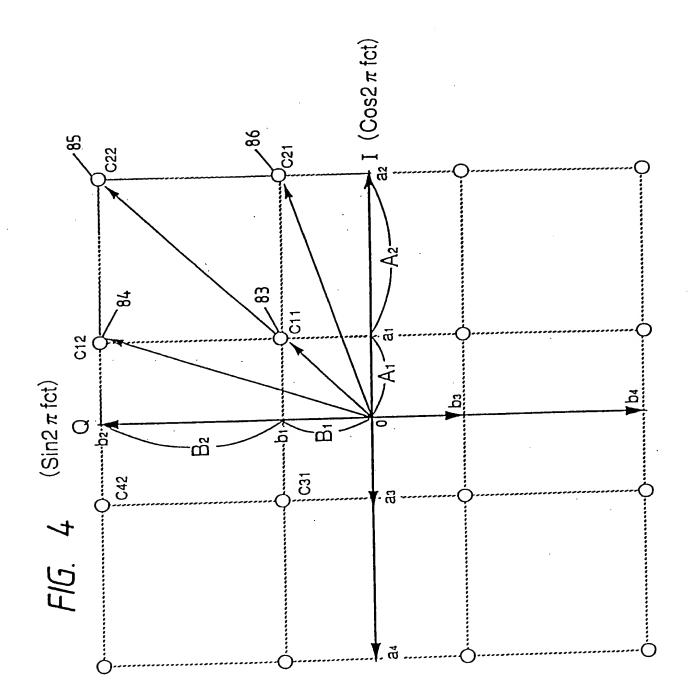


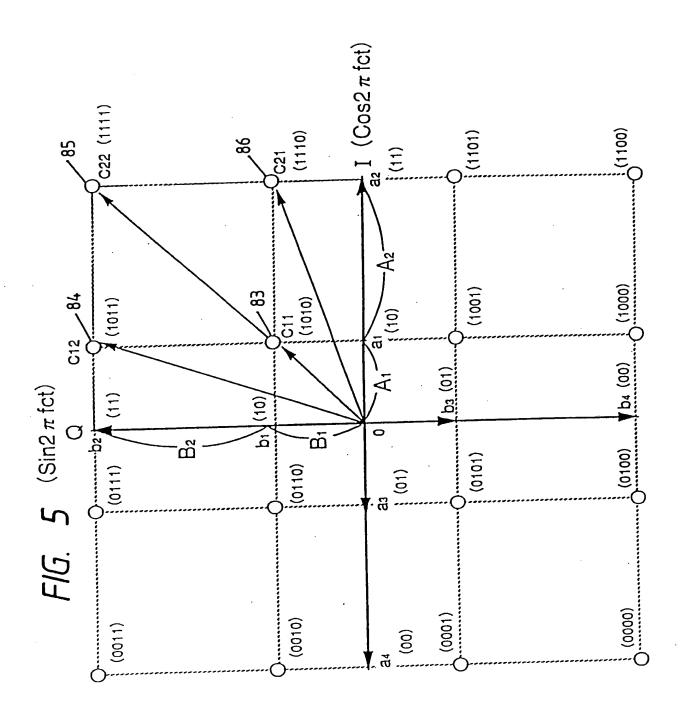
(//

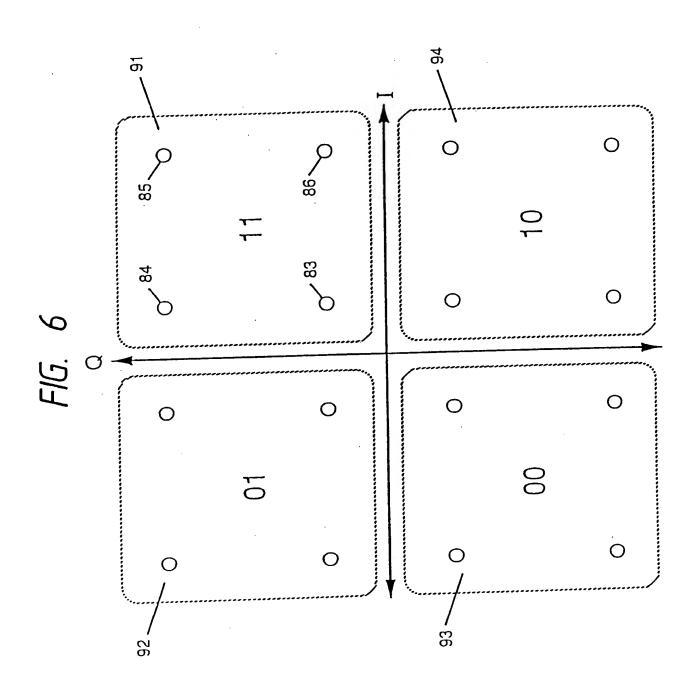
2

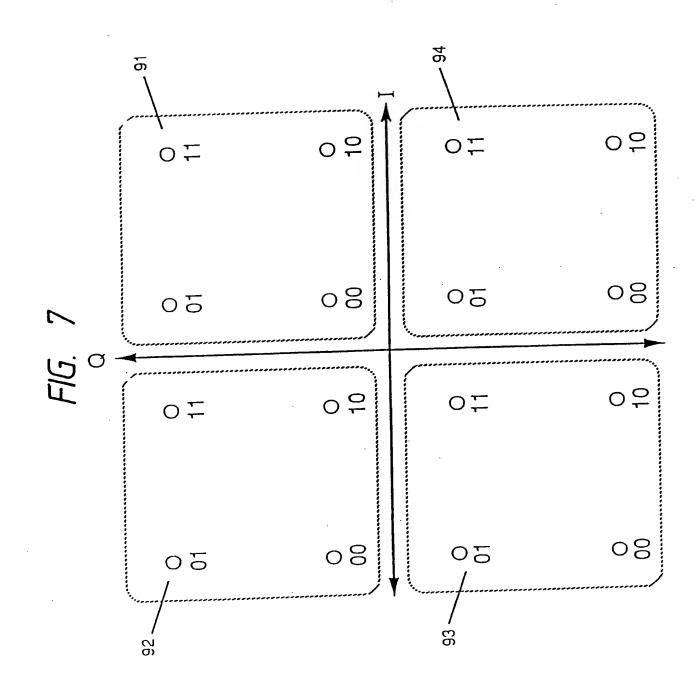


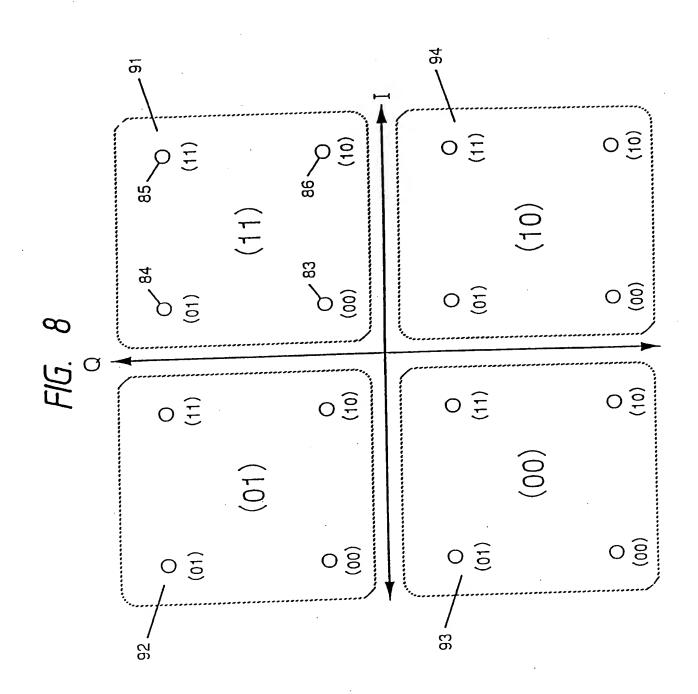


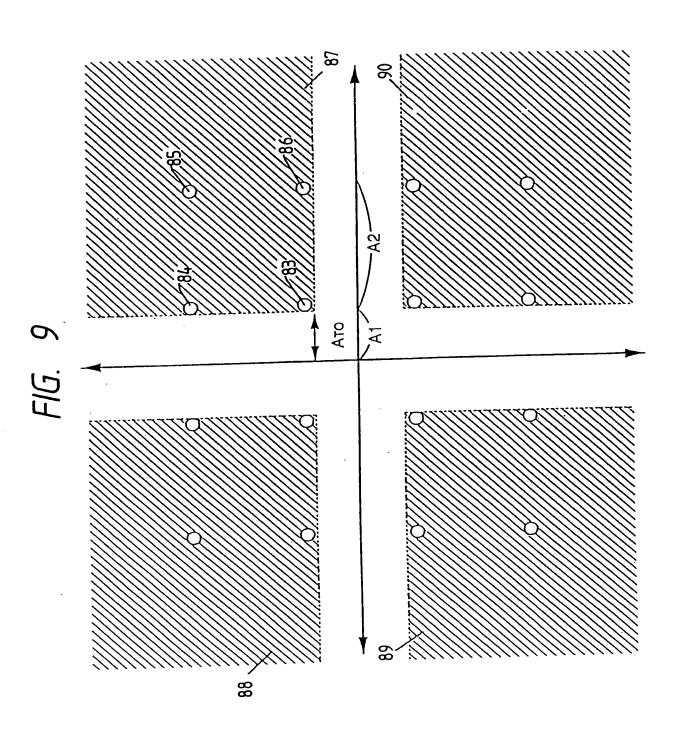


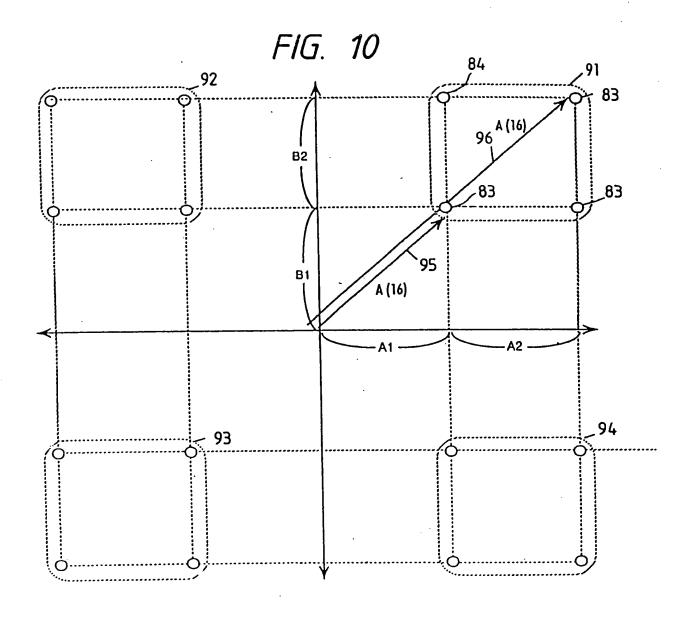












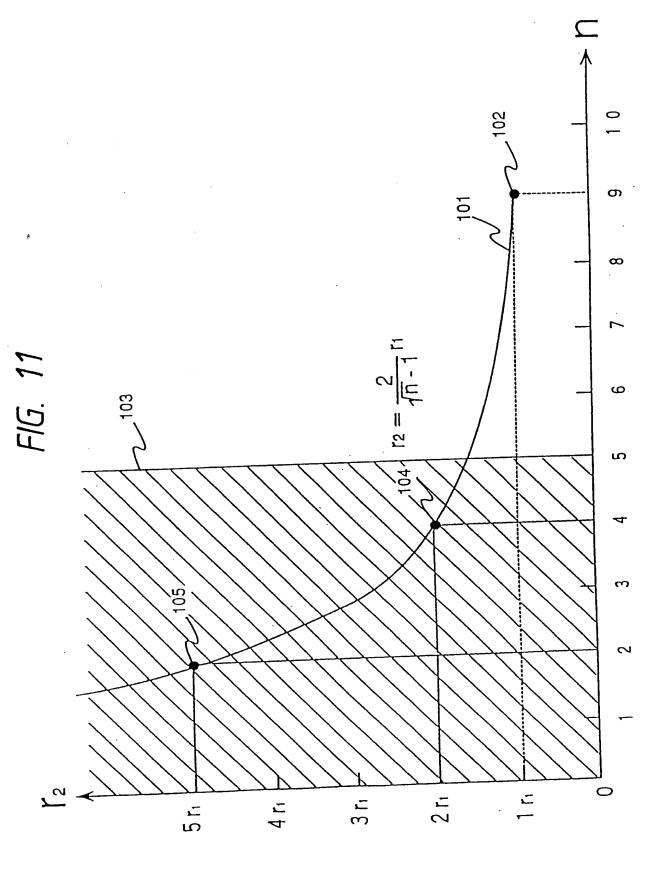


FIG. 12

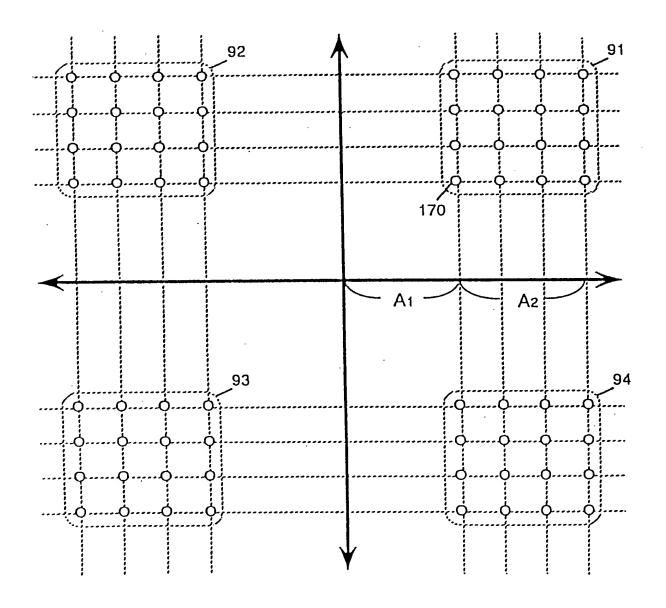
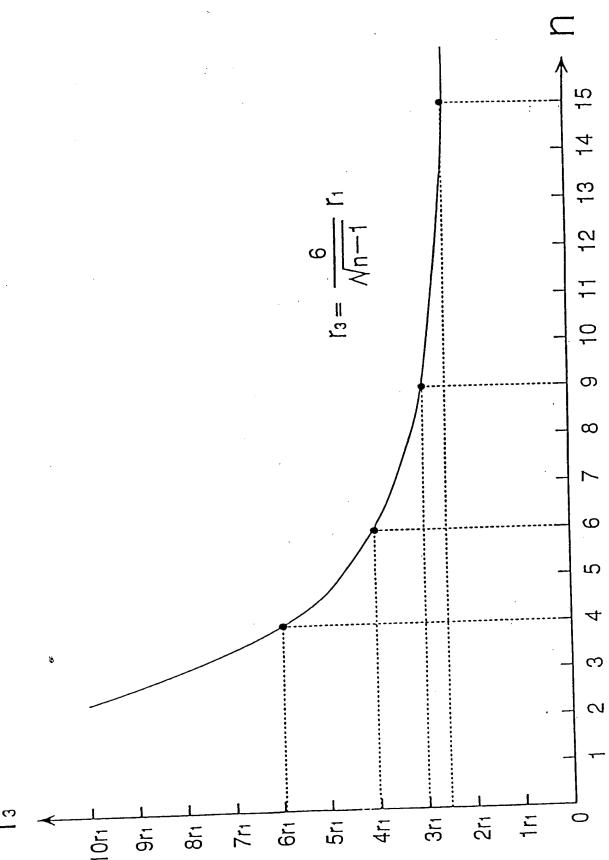
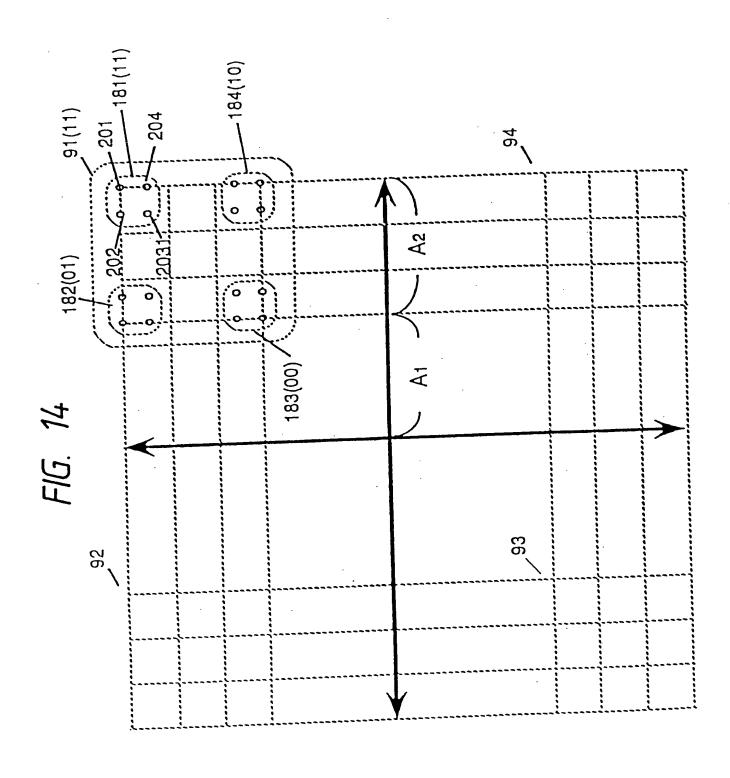
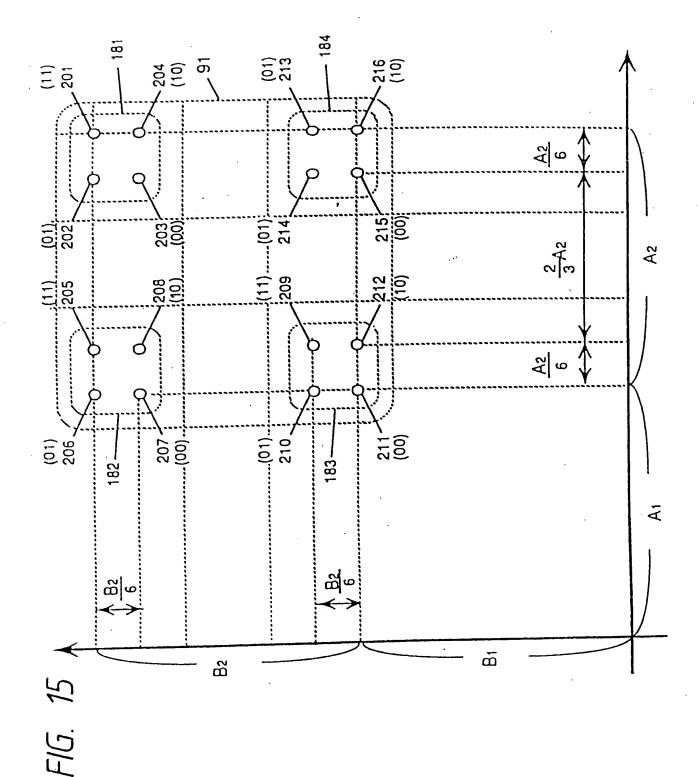
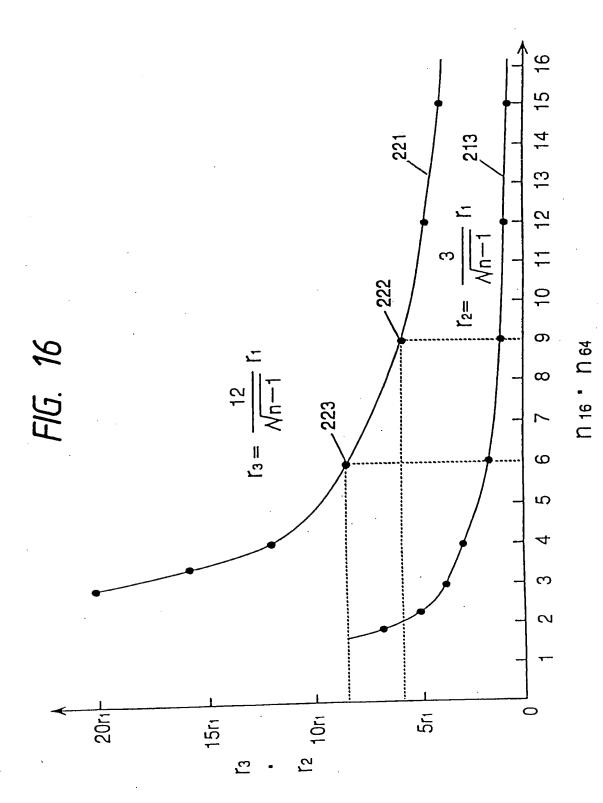


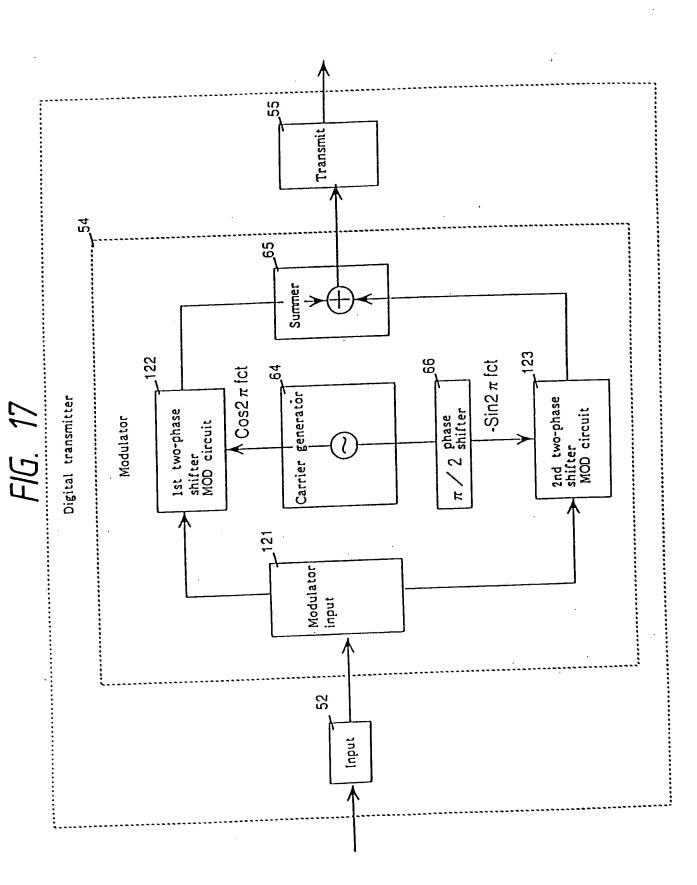
FIG. 13

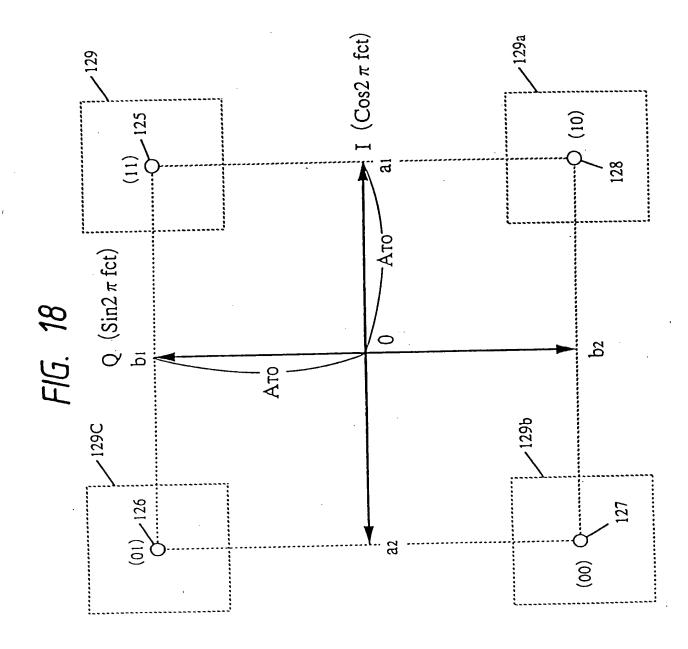


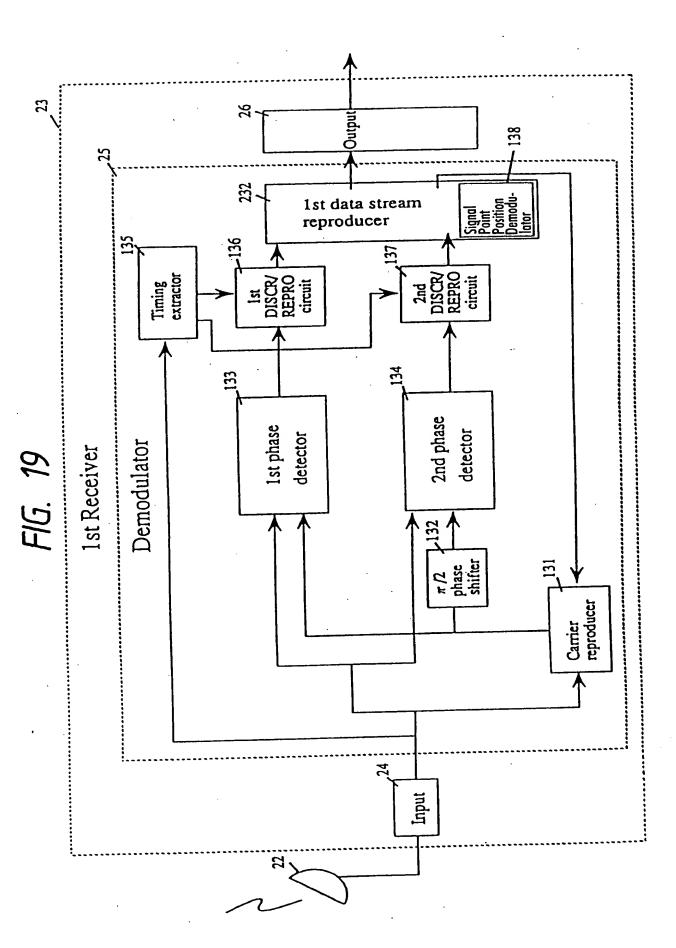


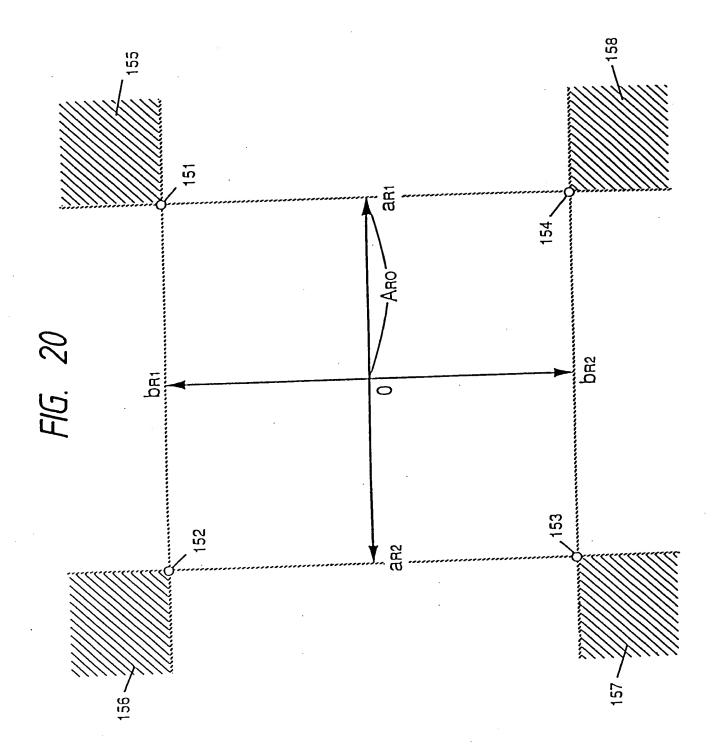


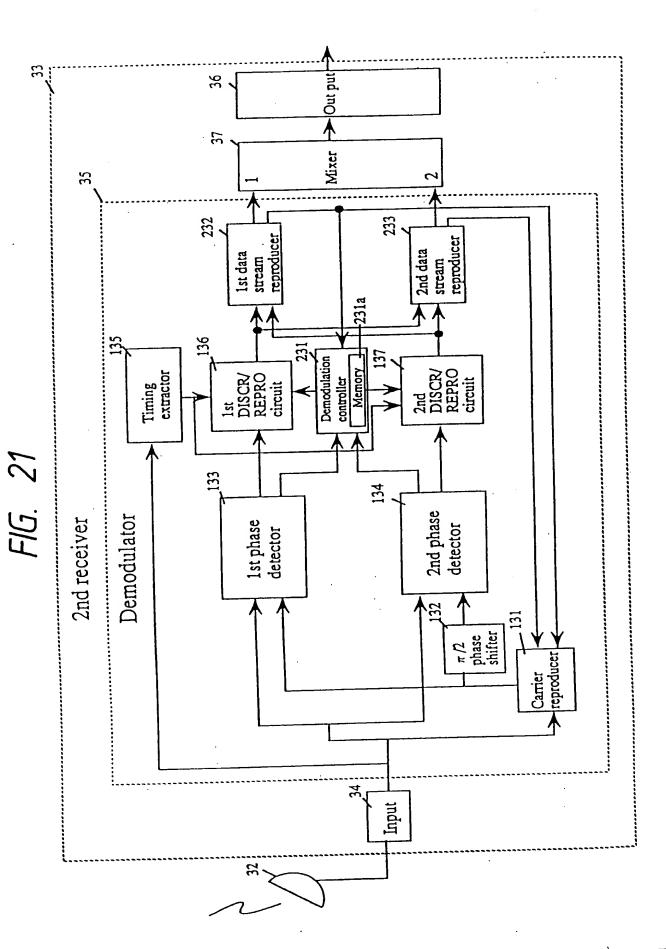


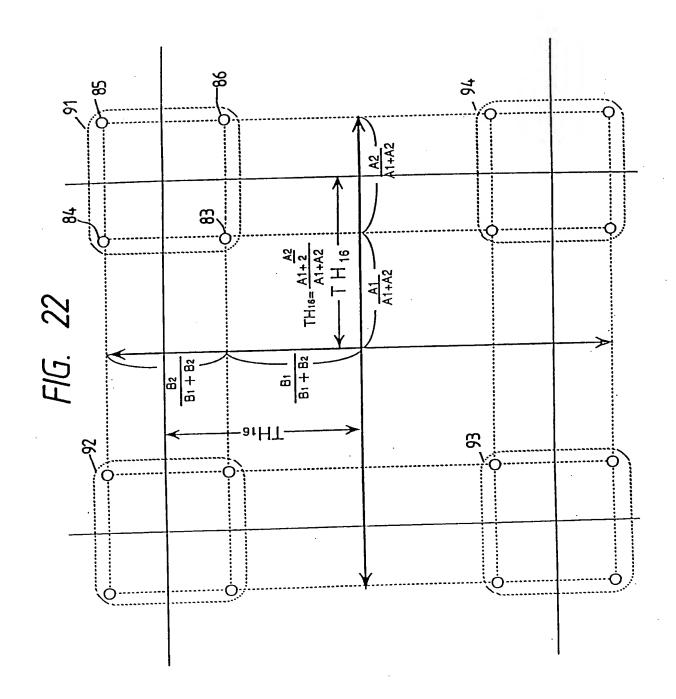


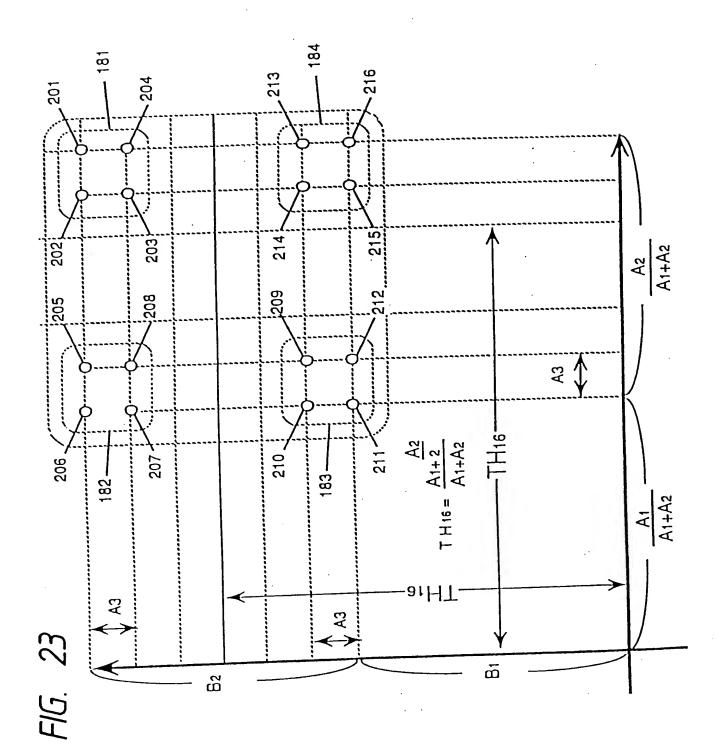


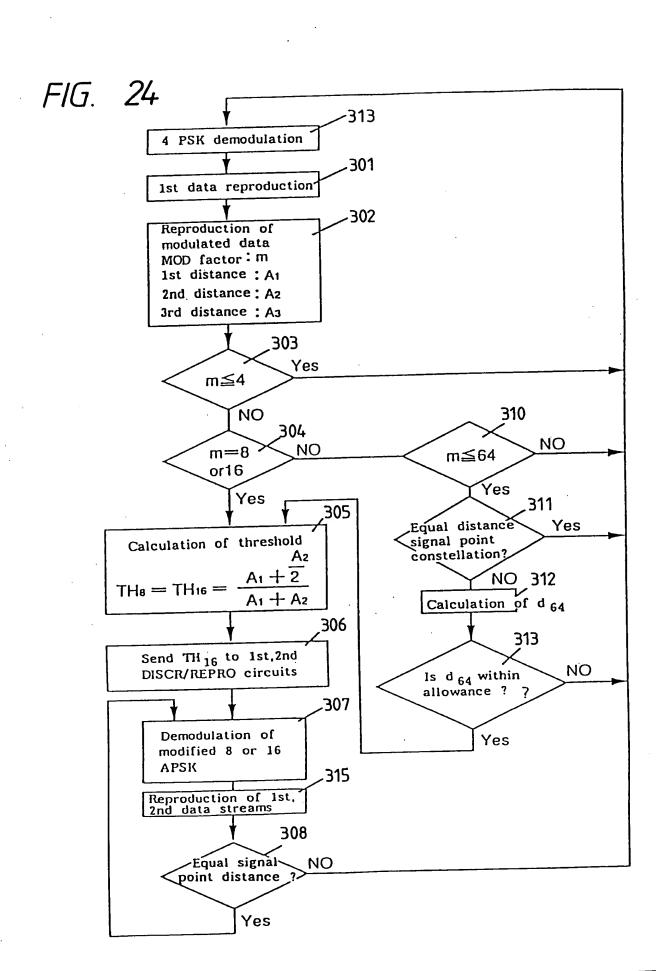












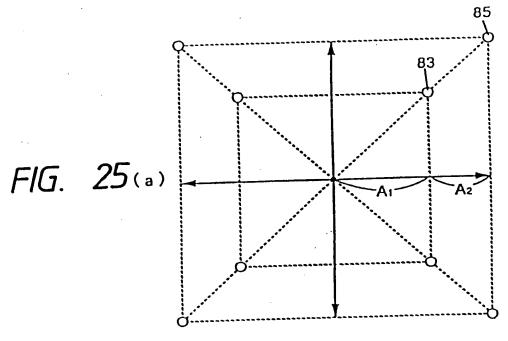
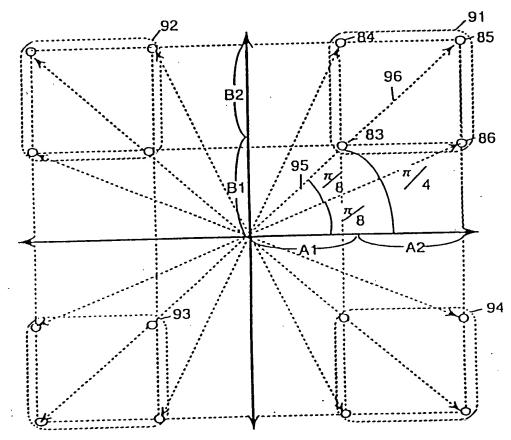
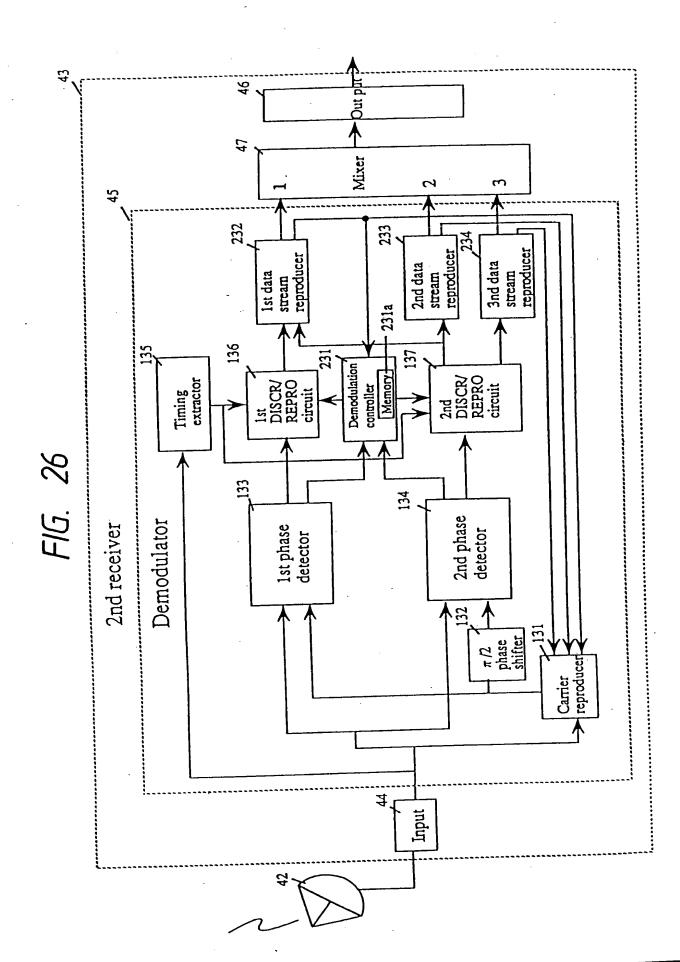


FIG. 25(b)





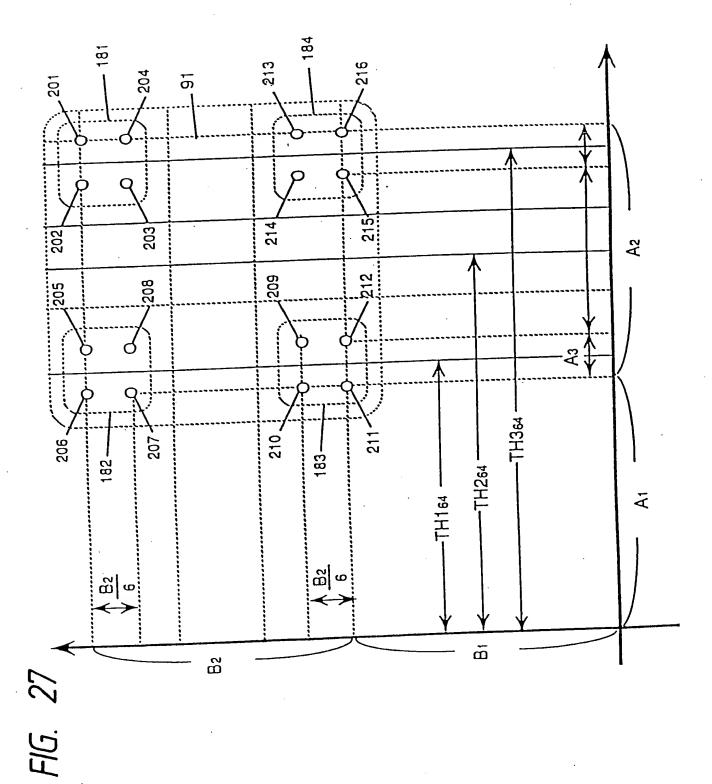
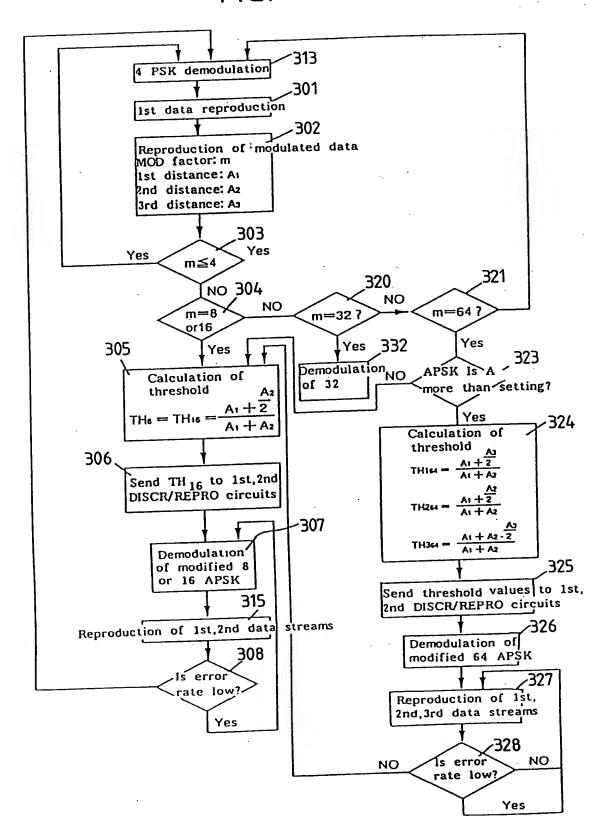
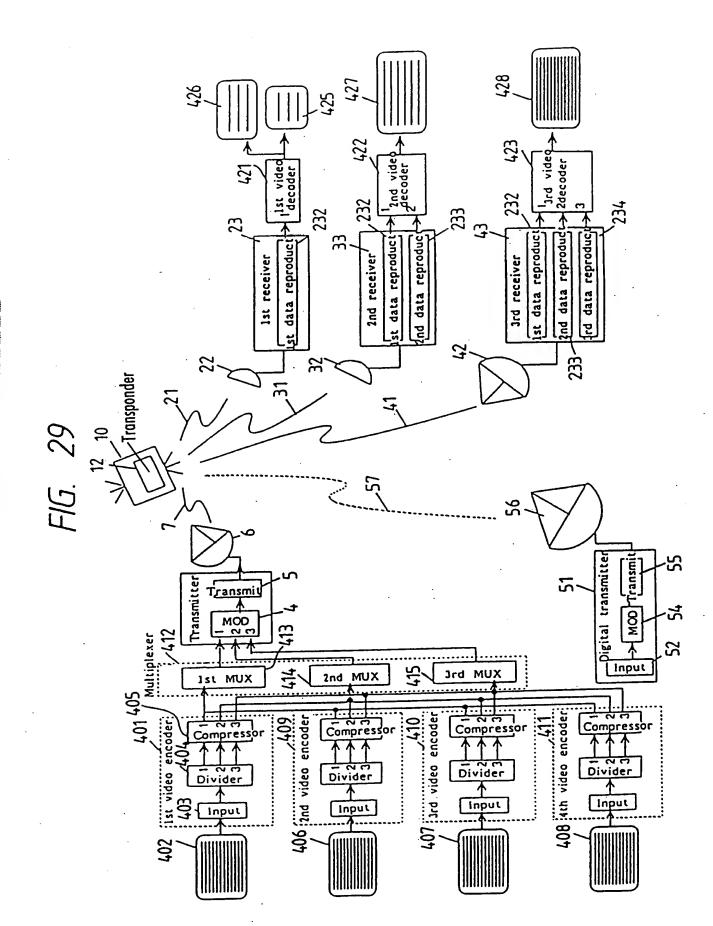
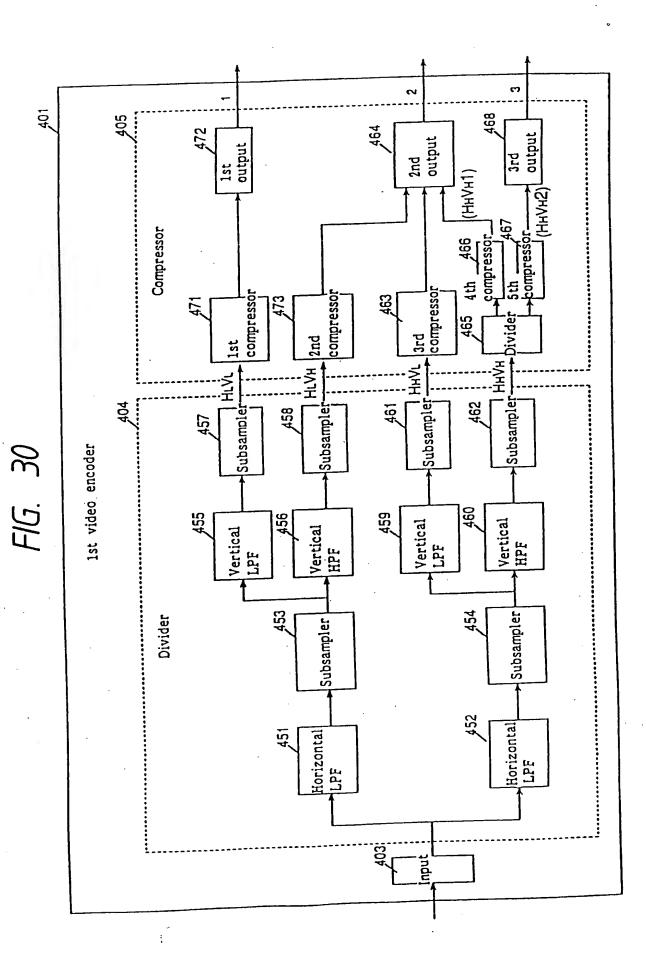


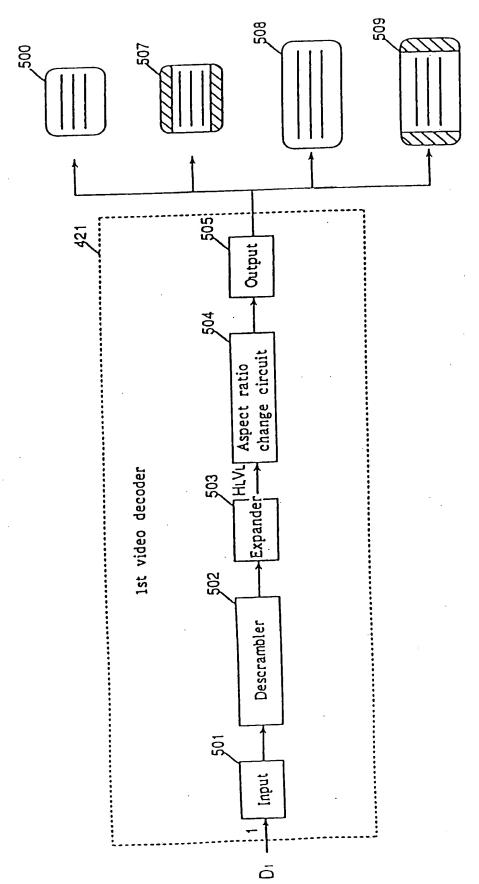
FIG. 28



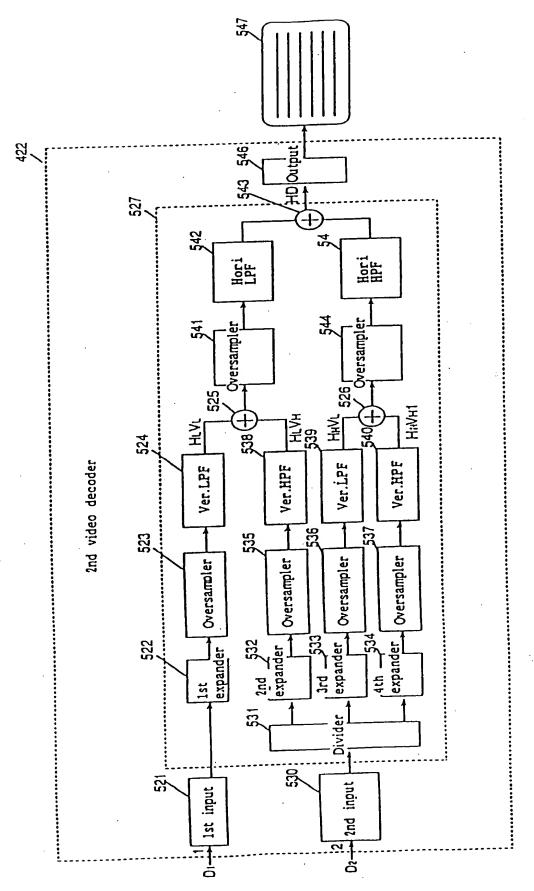


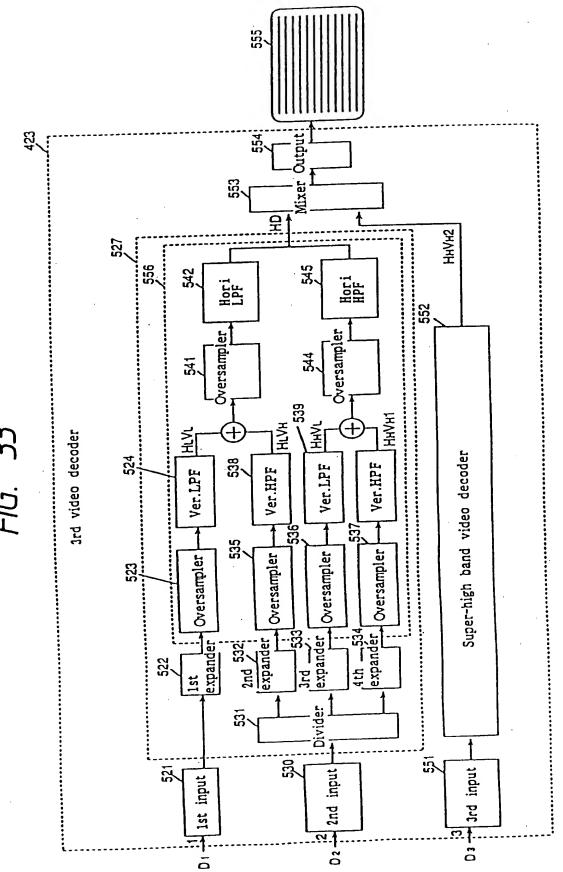


F16. 31



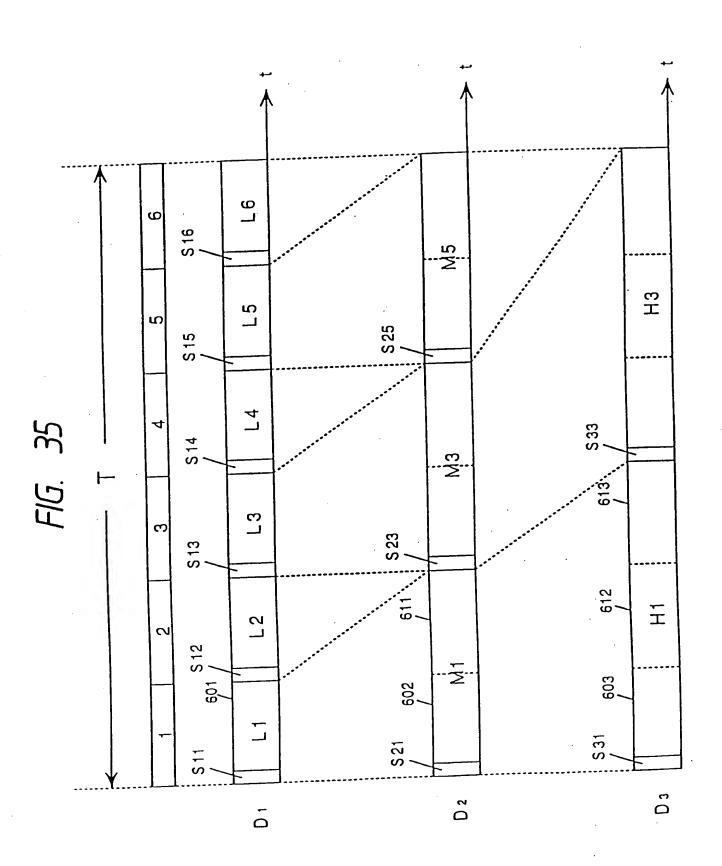
F1G. 32

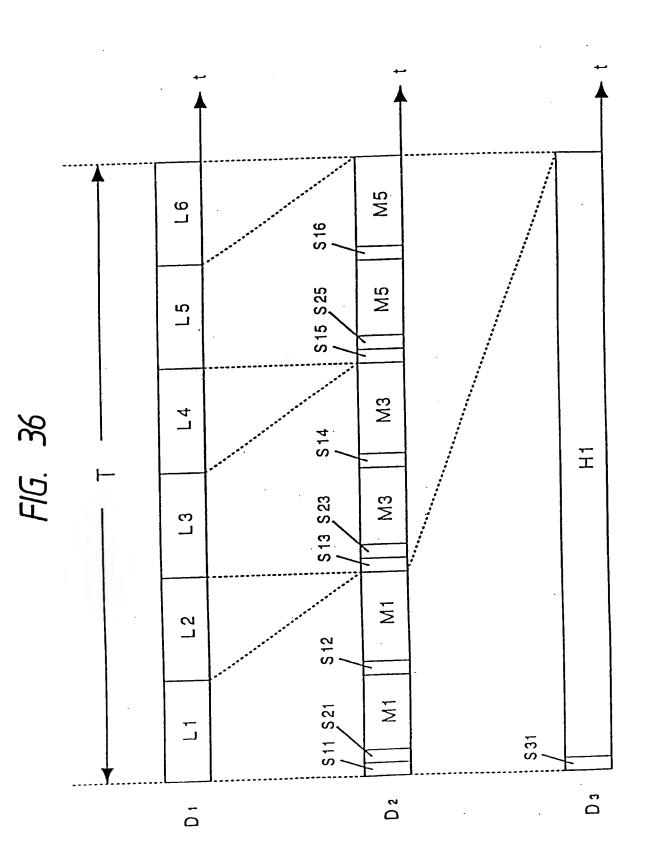




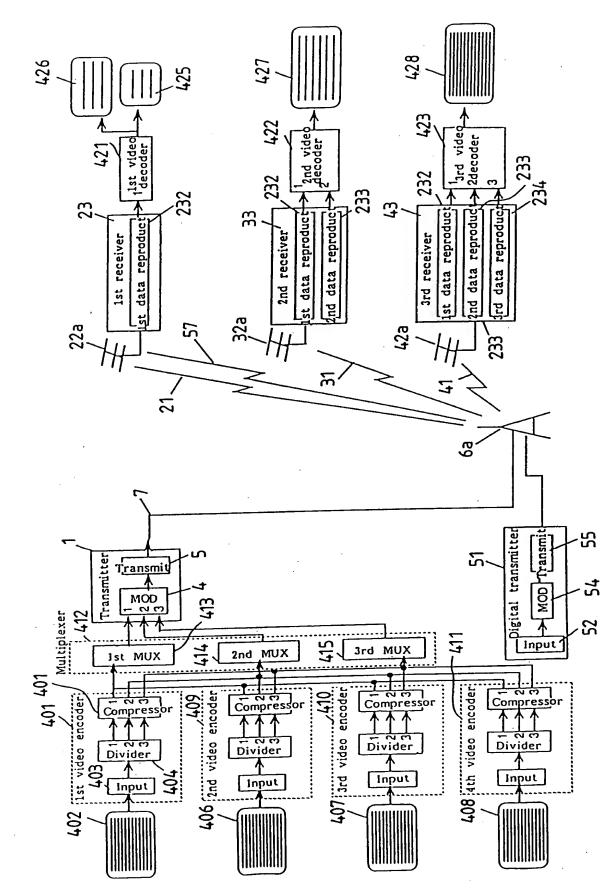
כר לו

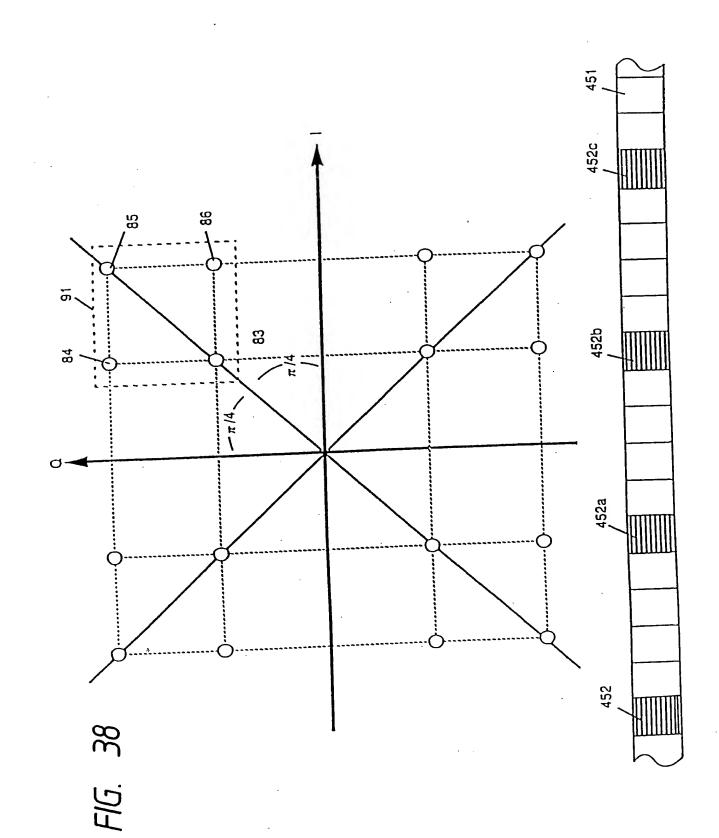
			<u></u>	· · · · · · · · · · · · · · · · · · ·	<b>+</b>
FIG. 34	ယ		L6	9 W	9 H
	2		L 5	Σ S	H5
	4		L 4	M	H4
	ဗ		L3	M3	H3
	2		L2	M S	H2
	-	109	-	M1 603	H1
	 	J		2	0

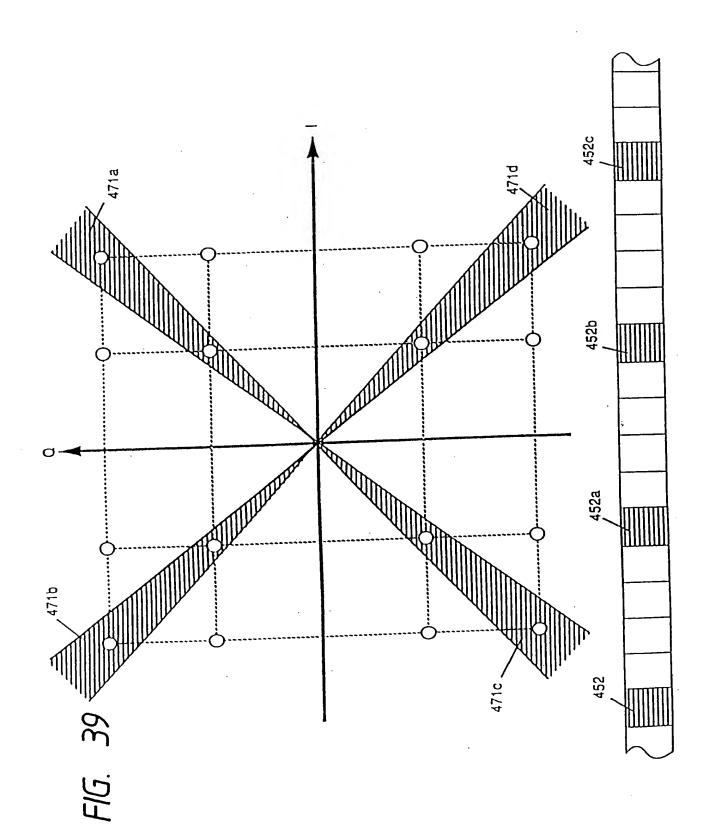




F1G. 37







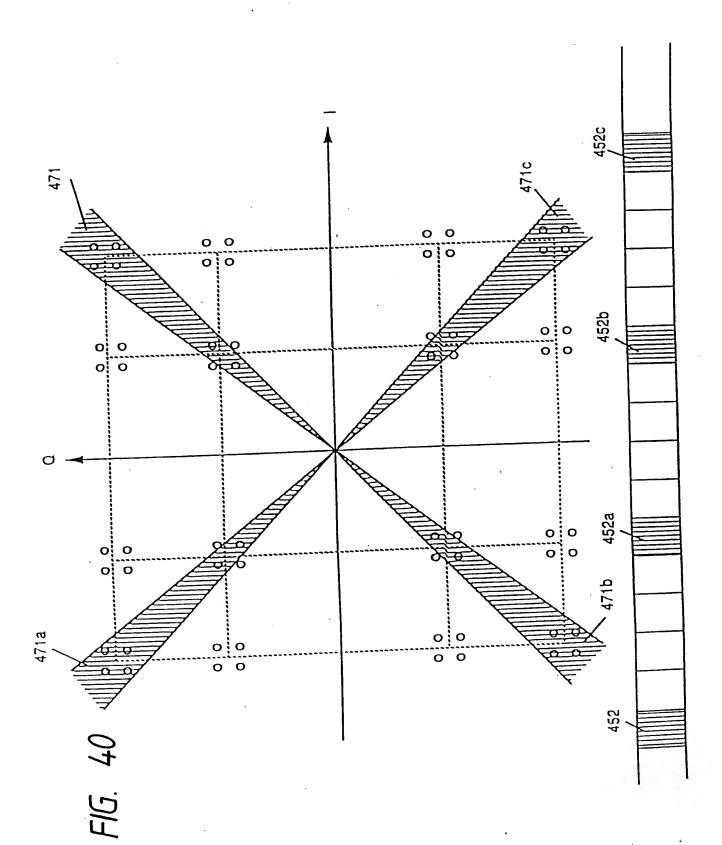
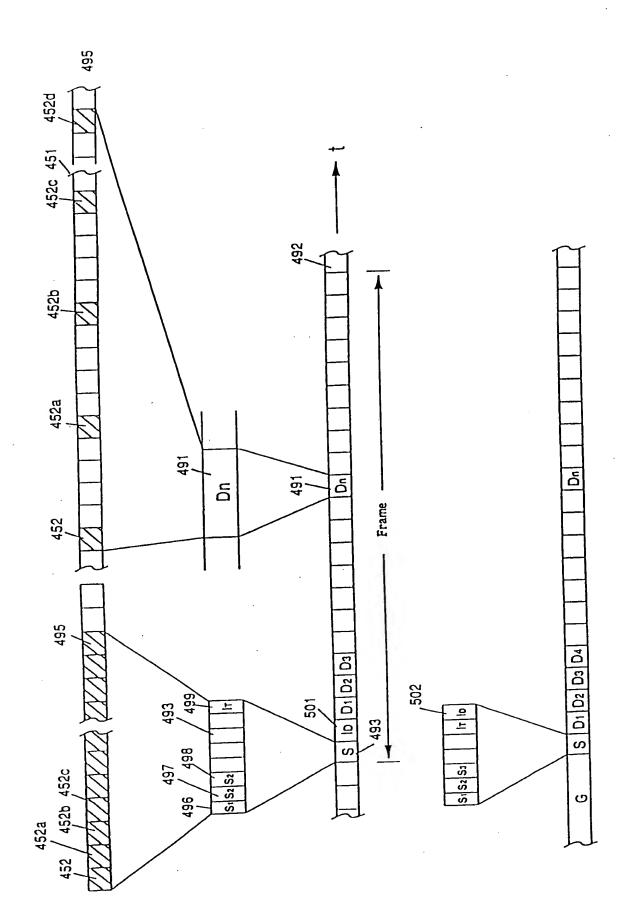


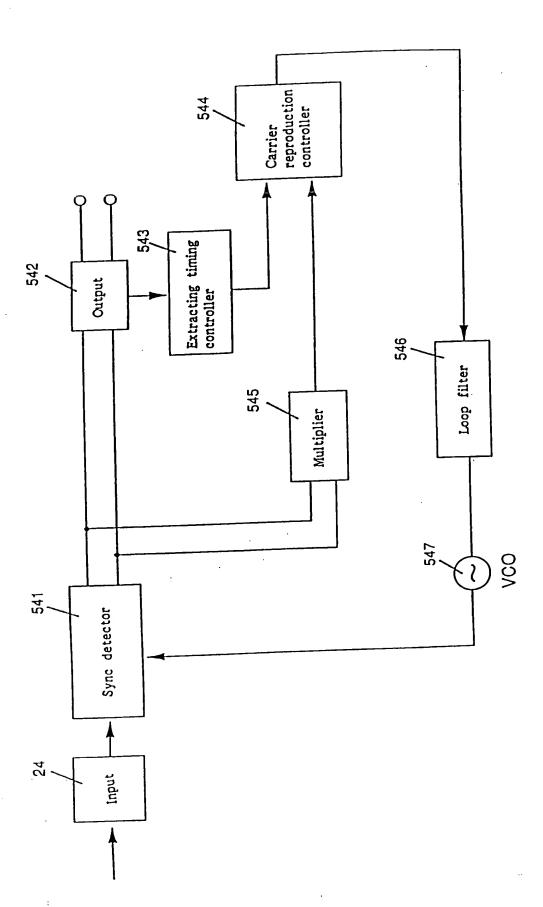
FIG. 41

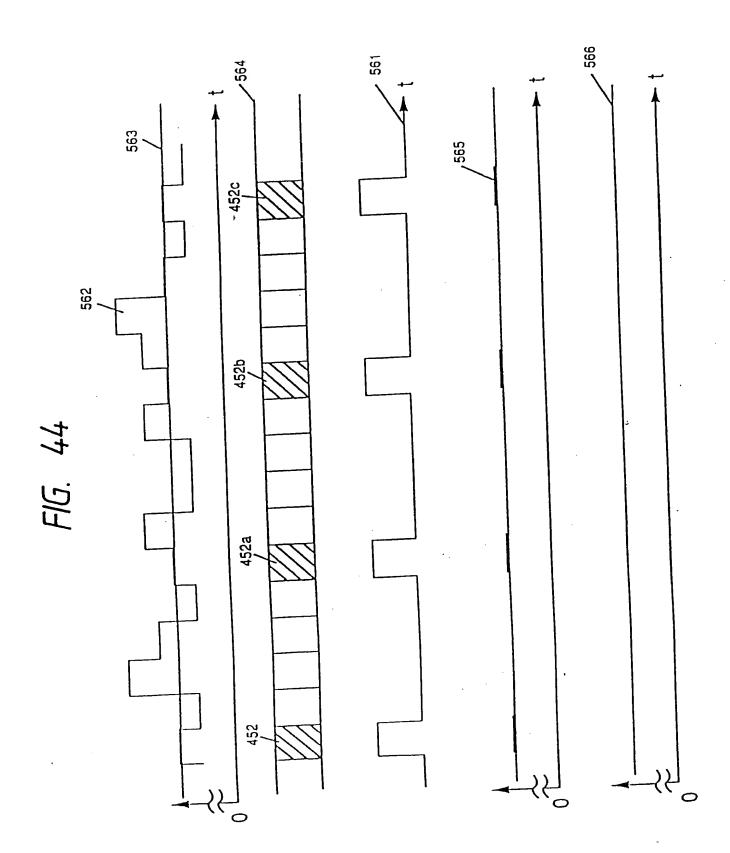


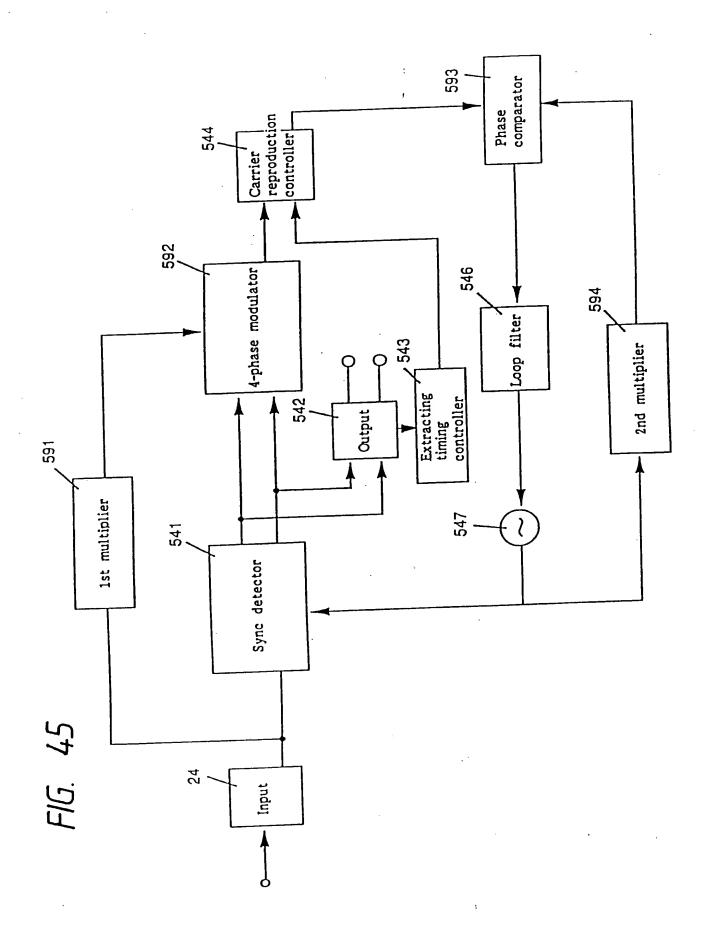
452i

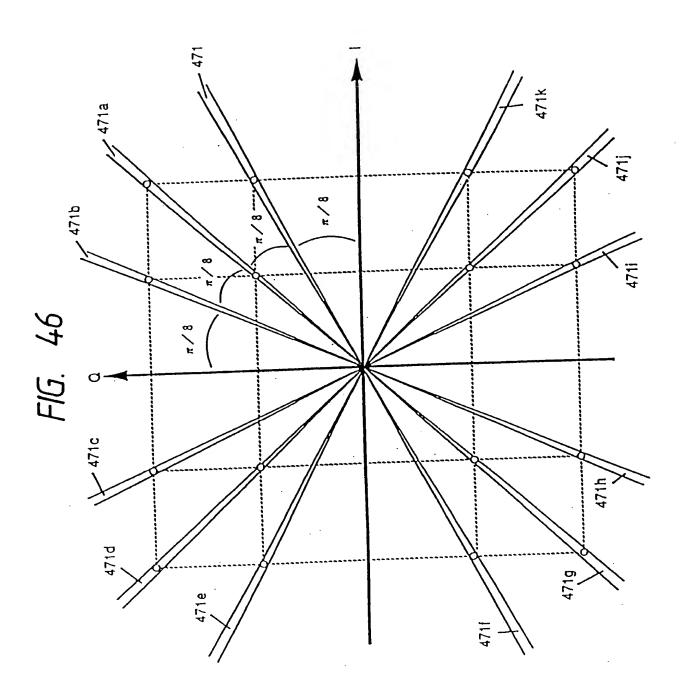
452h 522b 491b 0.42 452g 521b S S 522a 491a 0,+1 <u>†</u> 452d 452e 452f 521a S S 491 FIG. 42 ద్ ۵ 5 ద్ 522 Frame S 521 S | D1 | D2 | D3 | D4 | 10 D1 D2 D3 502 三 S S1 S2 S2 51 52 52 ပ 452 452a 452b

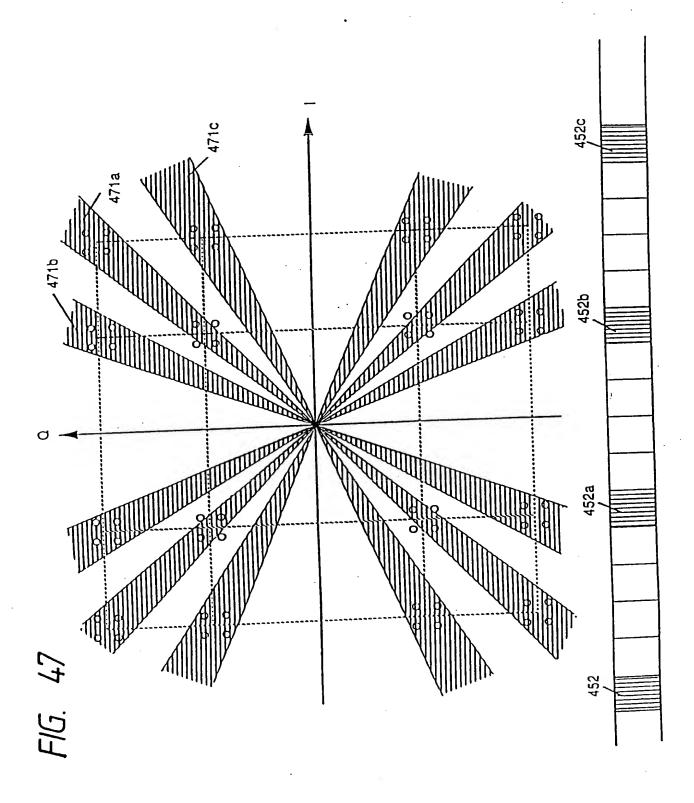
F1G. 43



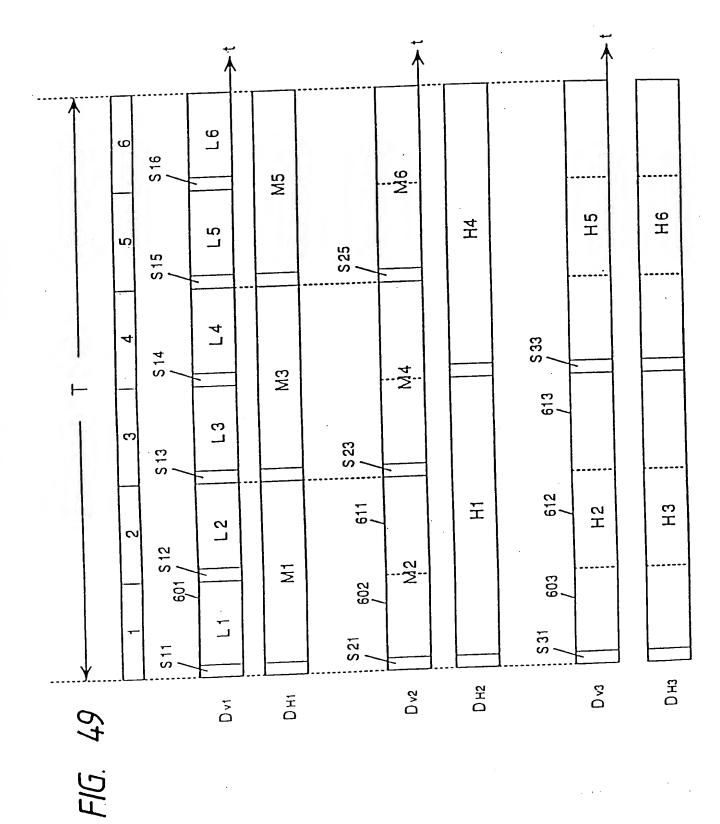


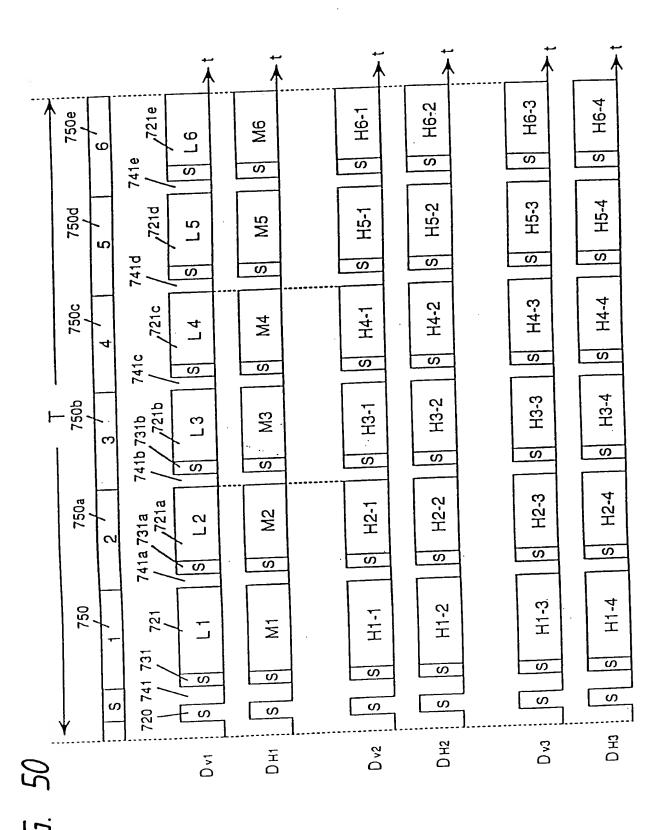






sin 16 8 661 670 673 672 671 Subtractor  $\cos 4\theta$ Adder  $\sin 4 \theta$ 545 Output 999 699 667 Subtractor 668 Adder 546 Loop filter 665 664 663 662 Subtractor Adder  $\theta$  soo sin  $\theta$ 547 VCD 541 Sync detector





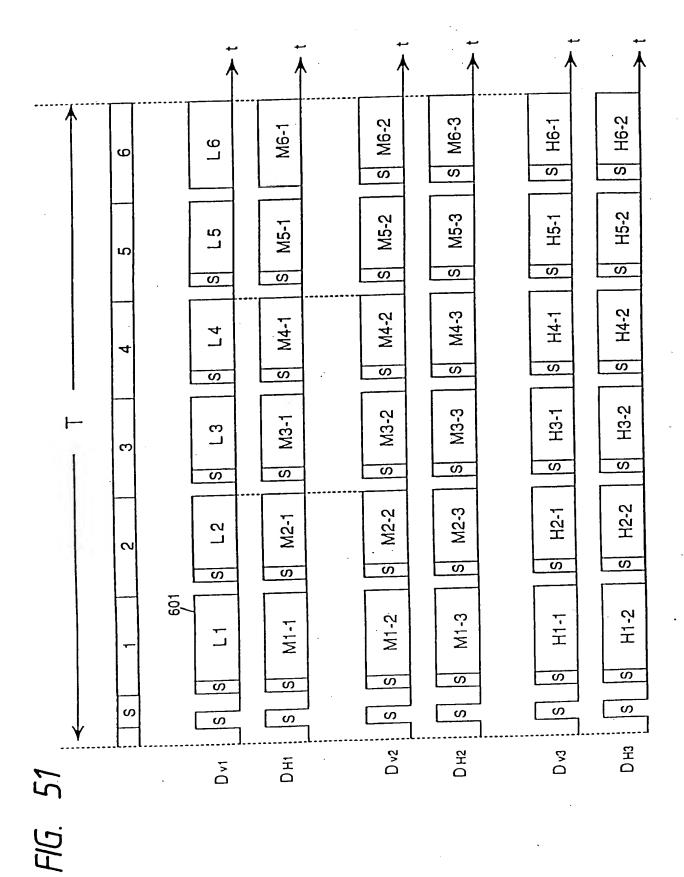


FIG. 52

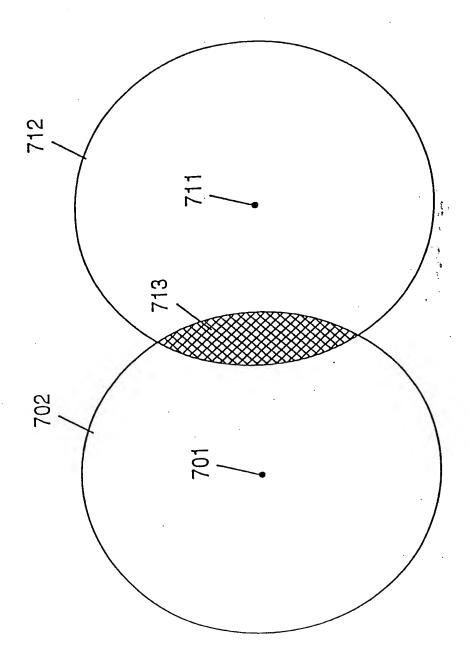


FIG. 53

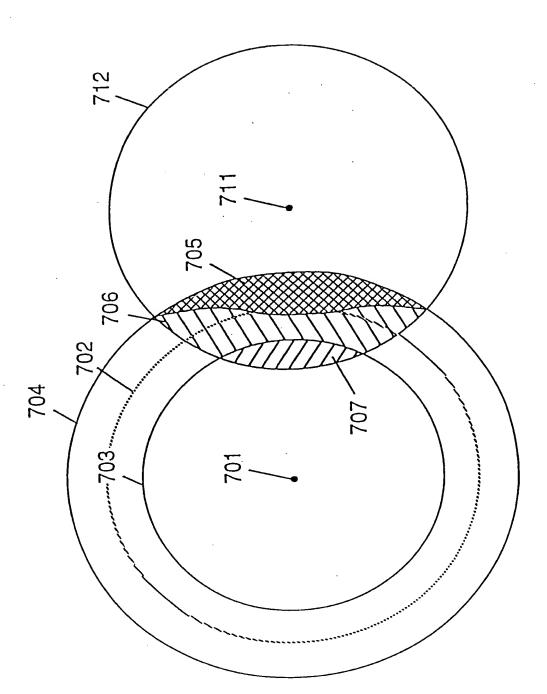


FIG. 54

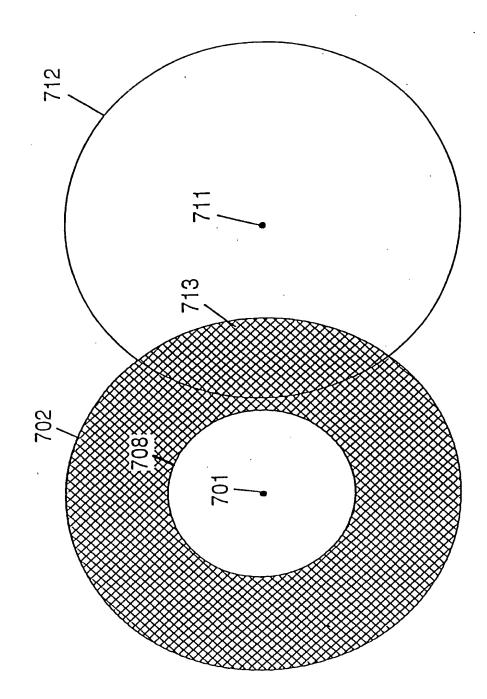
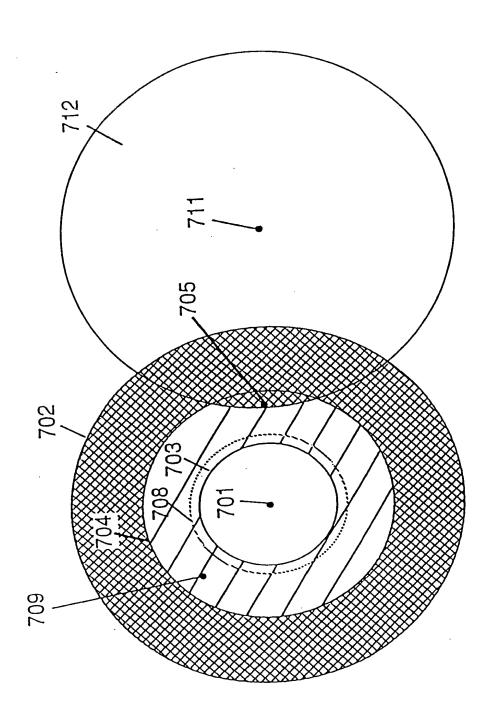
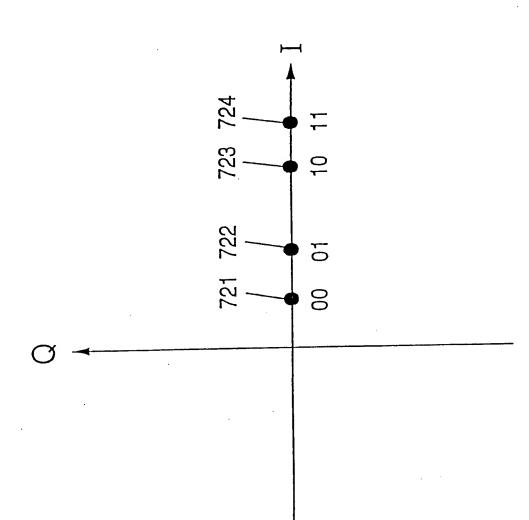


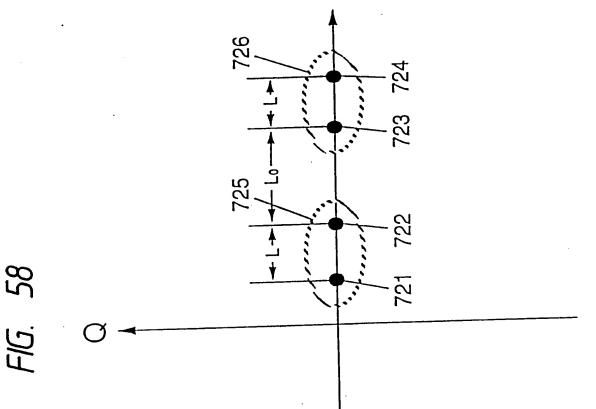
FIG. 55



704a 701a 714 FIG. 56 704 715 716 701 702

F1G. 57





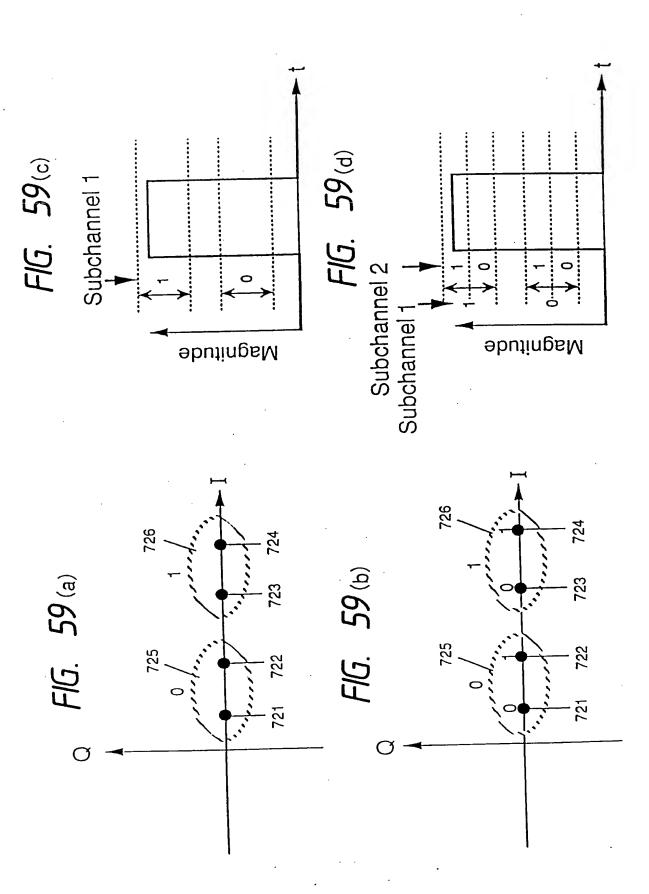
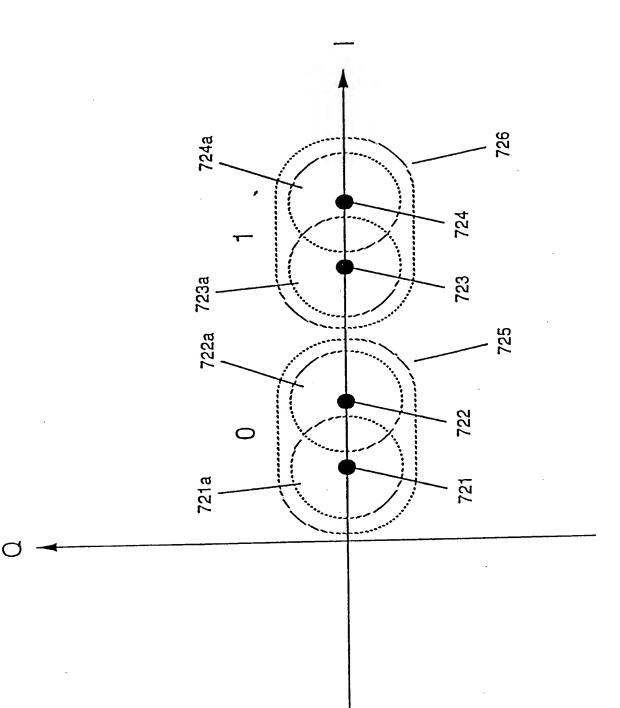
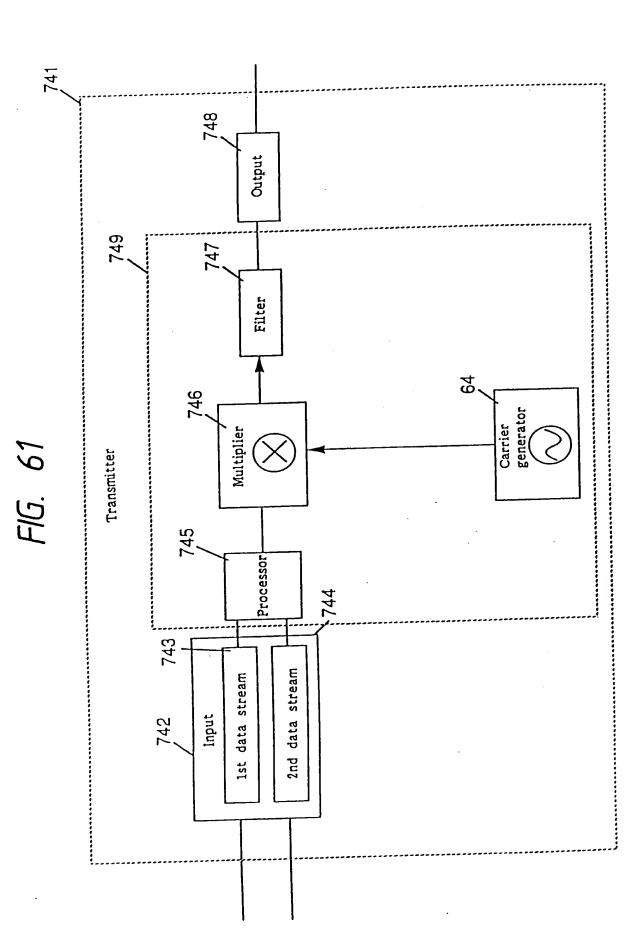
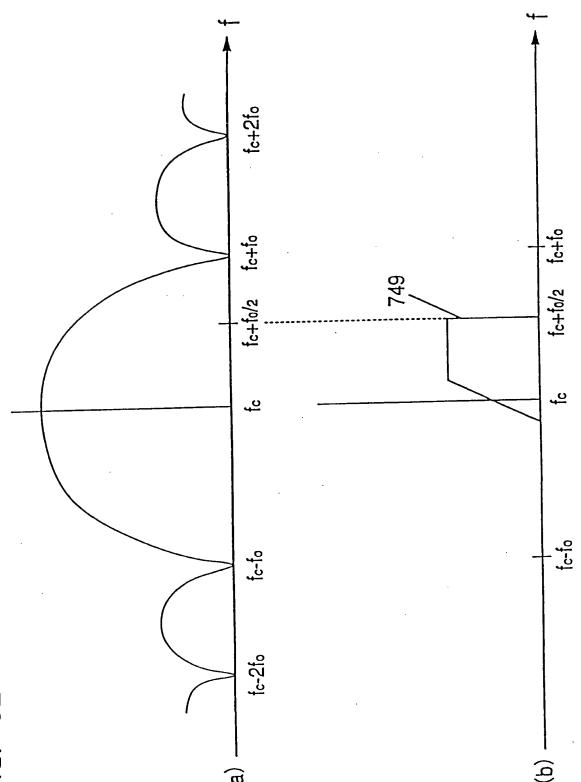


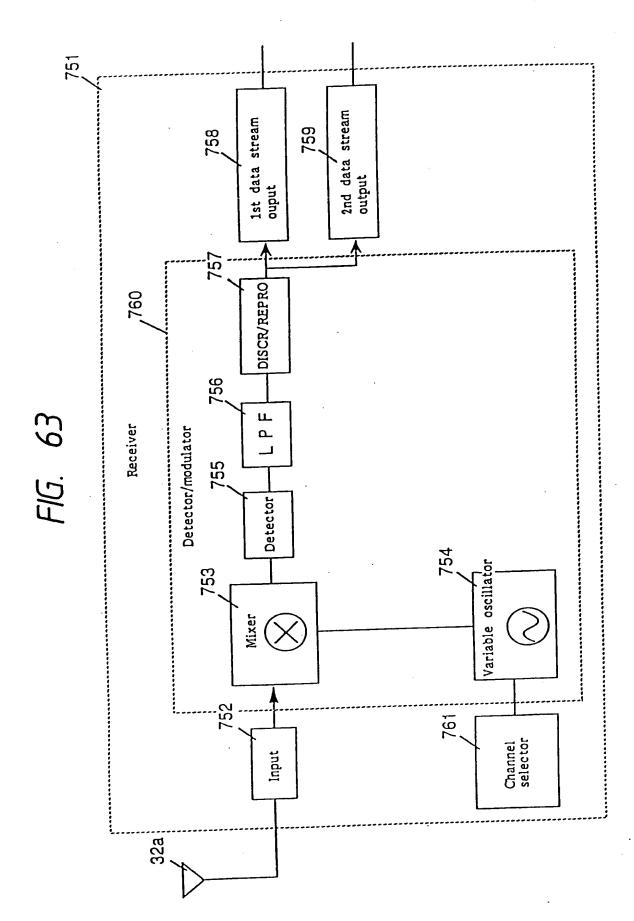
FIG. 60











Output 749 Modulator Transmitter ` 2nd data stream 1st data ,743 stream 744 773 Mixer 77. Mixer 772 Mixer Video signal transmitter 405 Compressor 401 1st video encoder HYH H.VH 光 H/L 404 Divider 403 Input

FIG. 64

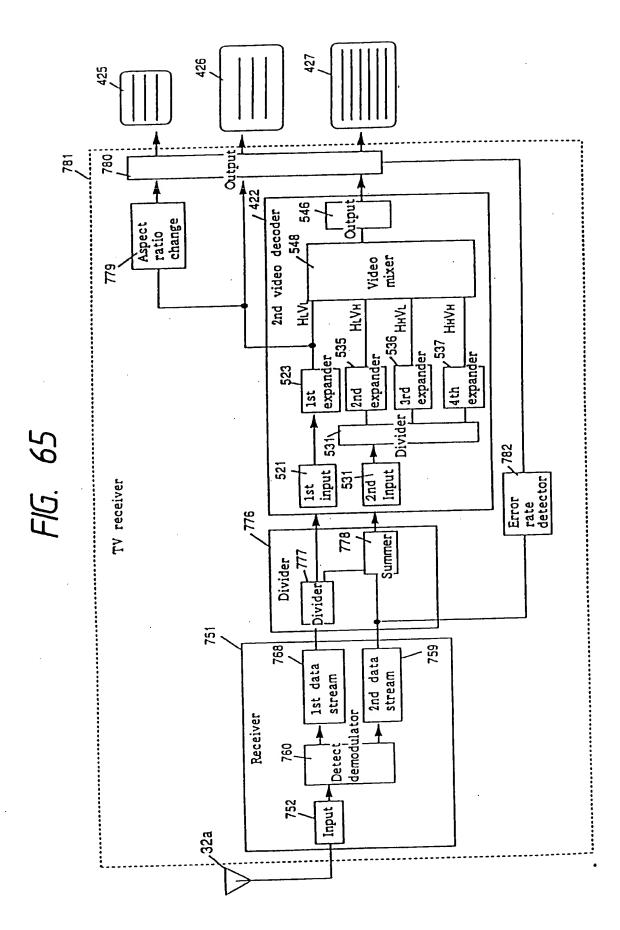


FIG. 66

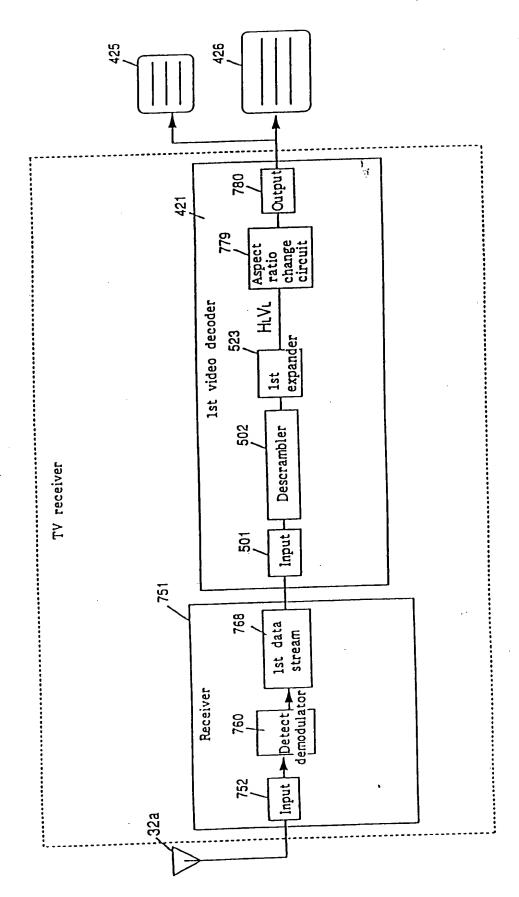
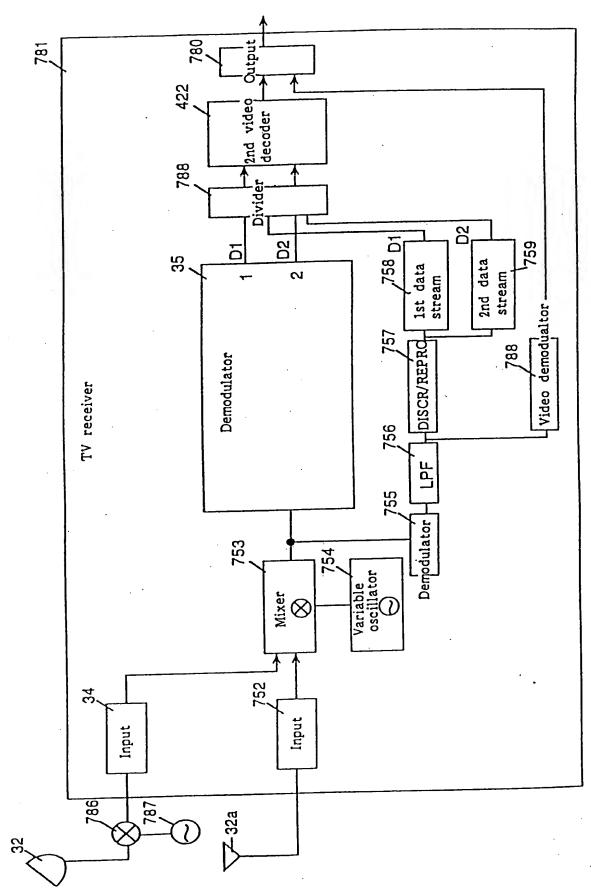
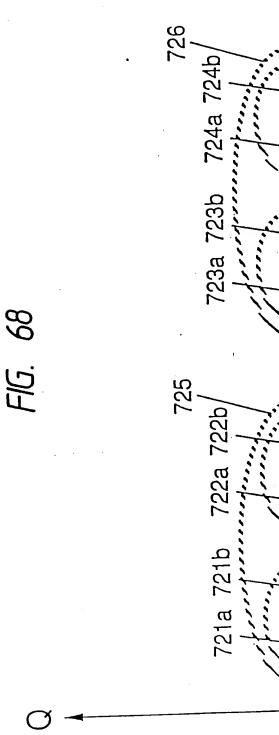
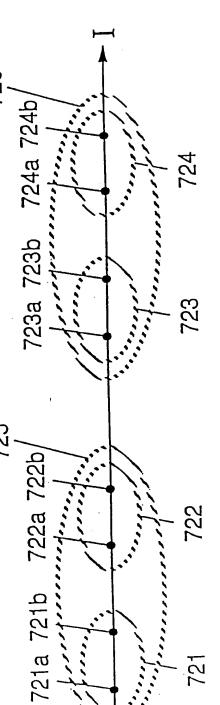


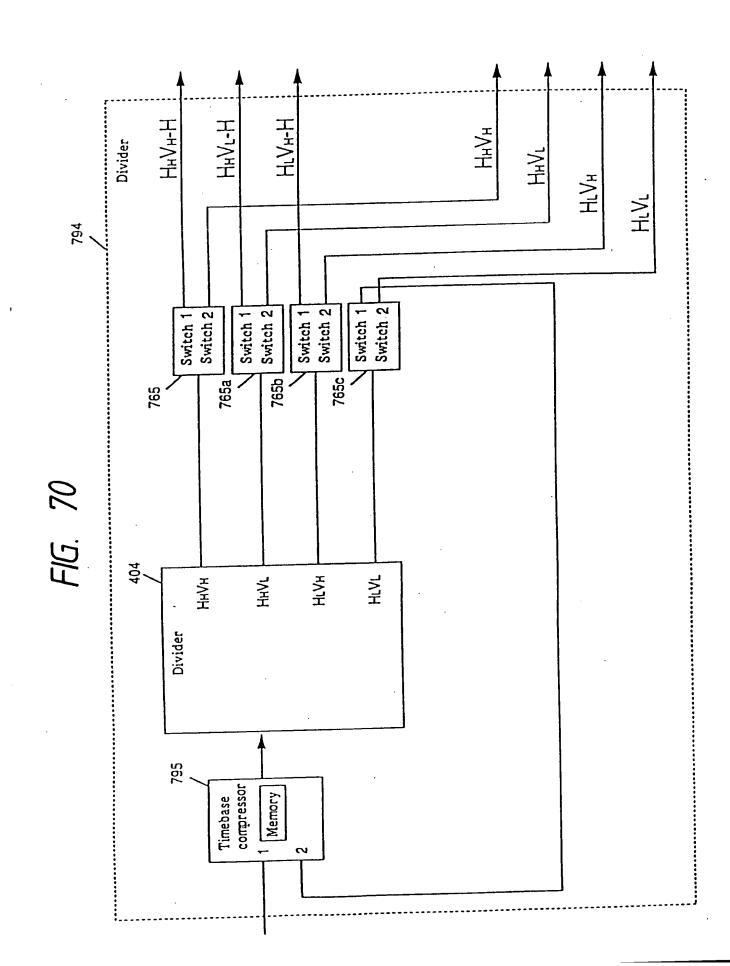
FIG. 67







 $\tilde{\Box}$  $\tilde{\Box}$ 401 472 464 468 2nd output 1st output 3rd output Compressor 471a 471d ,471g 471e 2nd compressor 4th compressor 3rd compressor 5th compressor 7th compressor 6th compressor НГУГ-Н 794 Нн/н-н **VHVL-**н Н. Ин-н 404a H\_VH-H Нн/н-н VHVL·H FIG. 69 1st video encoder НСУНФ H\_VL HHVHO VHVL Divider Divider H\_VH-H 404 FVL 4 НнVн ф VHVL 9 HLVH O Divider 403 Input



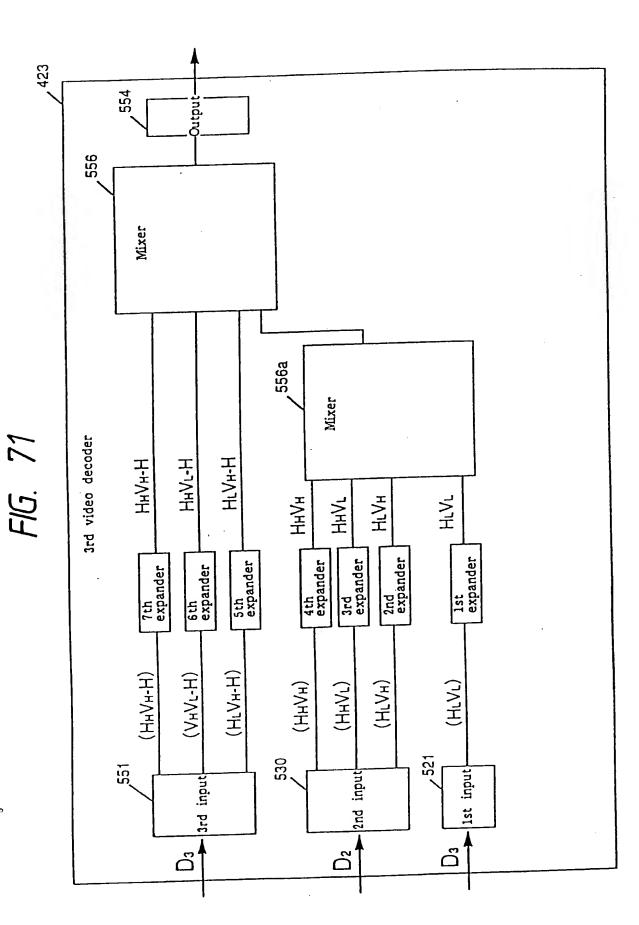


FIG. 72

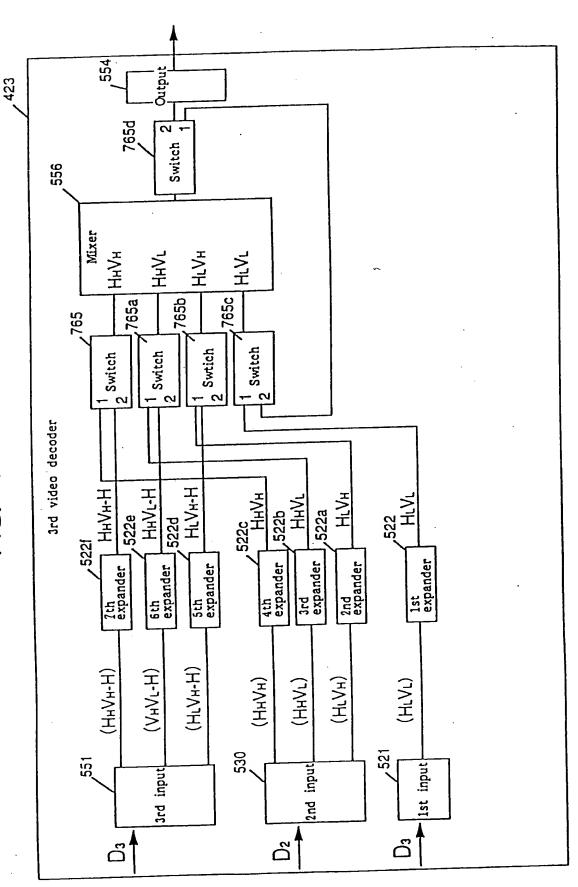


FIG. 73

		<b></b>			•
	· 		 †		tia ti4
					<b>t</b> 13
					<b>t</b> 12
Н-илн-Н		·			ts tro trr trz
			2		t10
Н-1/ИН			Timing 2		्ठ
					±®
H-N-H		·			4
					<b>⊸</b> 9
		·		, 	<u> </u>
		НнУн			4
		HHVL	: Timing 1		क्
		HLV <sub>H</sub>			고
	·	<u> </u>			-=
	<u> </u>	H/V <sub>L</sub>	<u> </u>	<u> </u>	ع. ا
2				-	

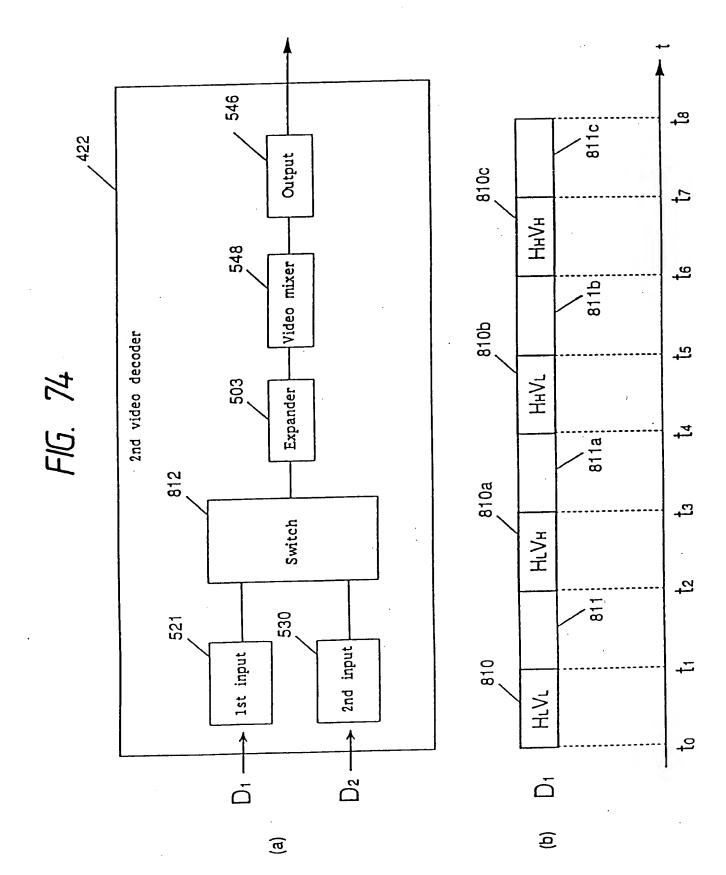


FIG. 75

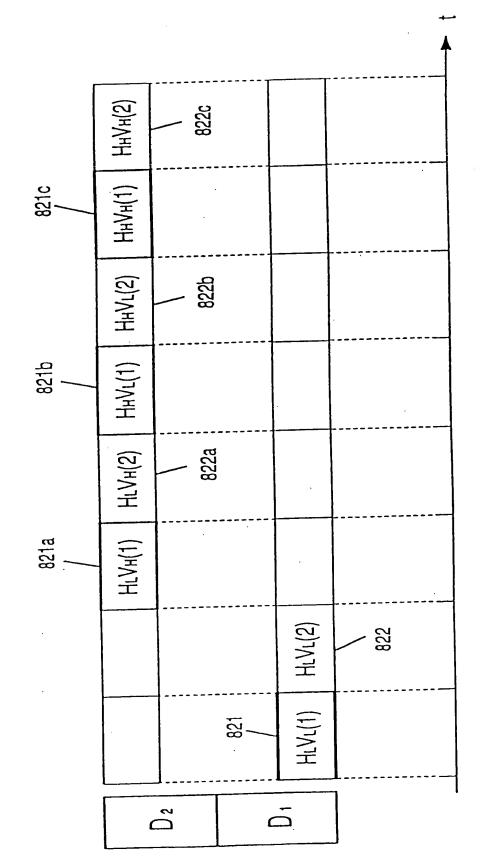


FIG. 76

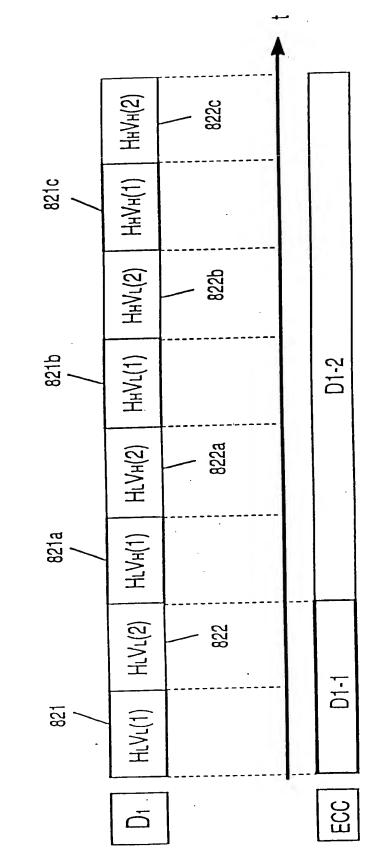
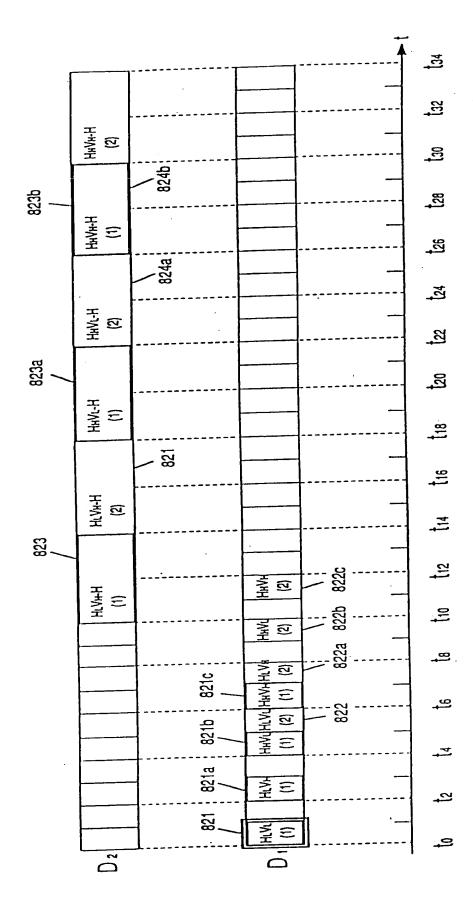


FIG. 77



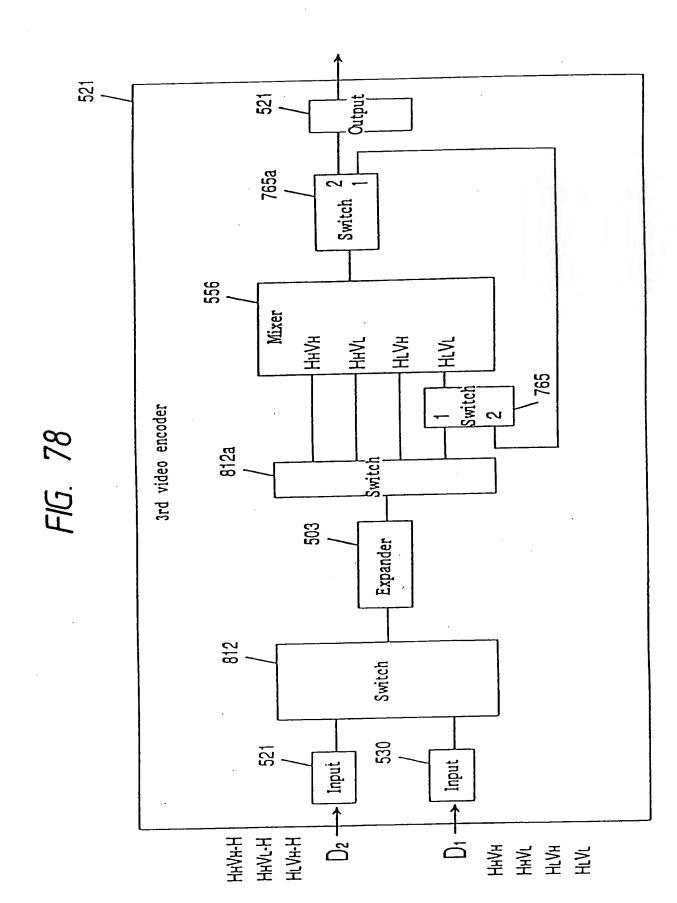
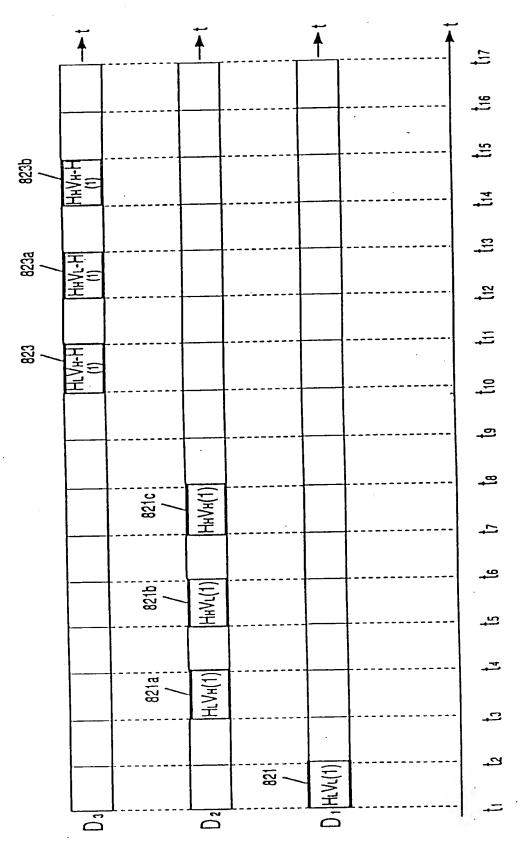


FIG. 79



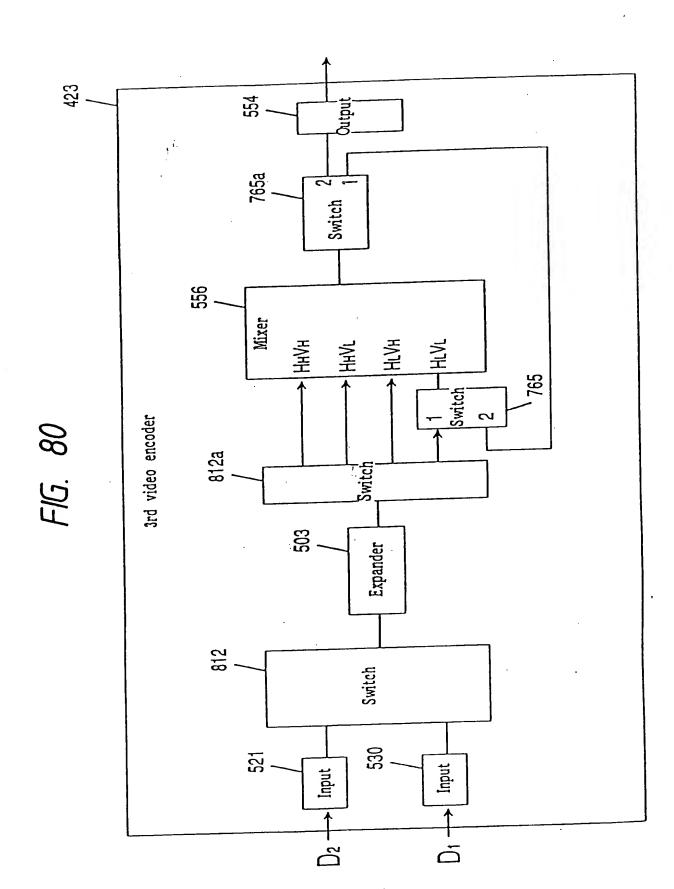
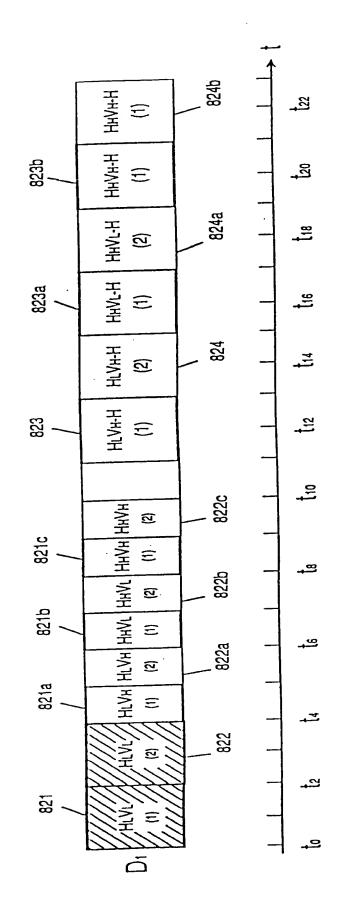


FIG. 81



F1G. 82

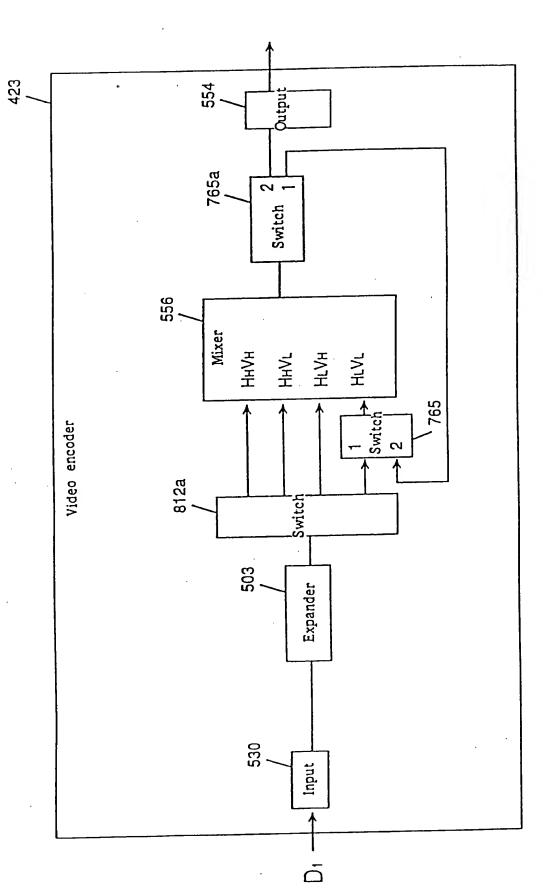


FIG. 83

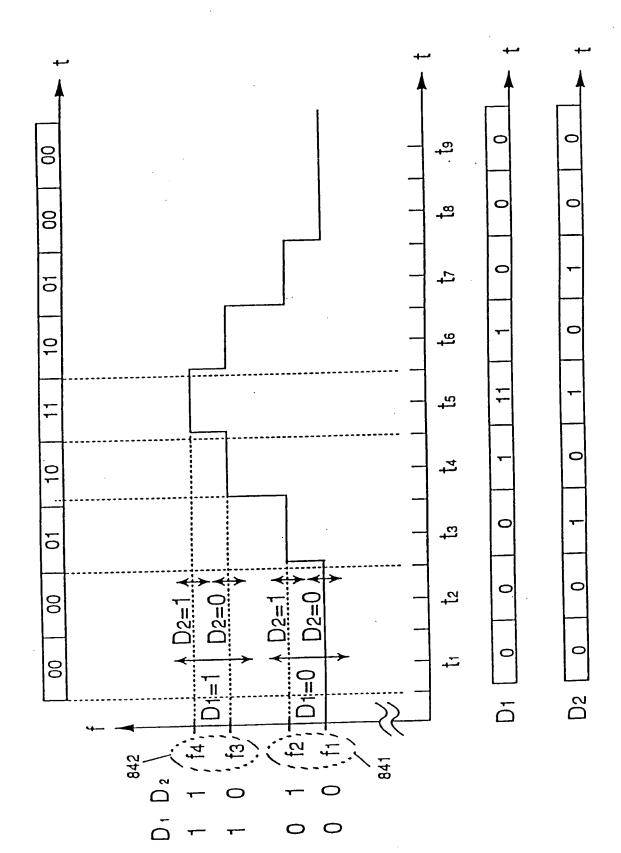


FIG. 84

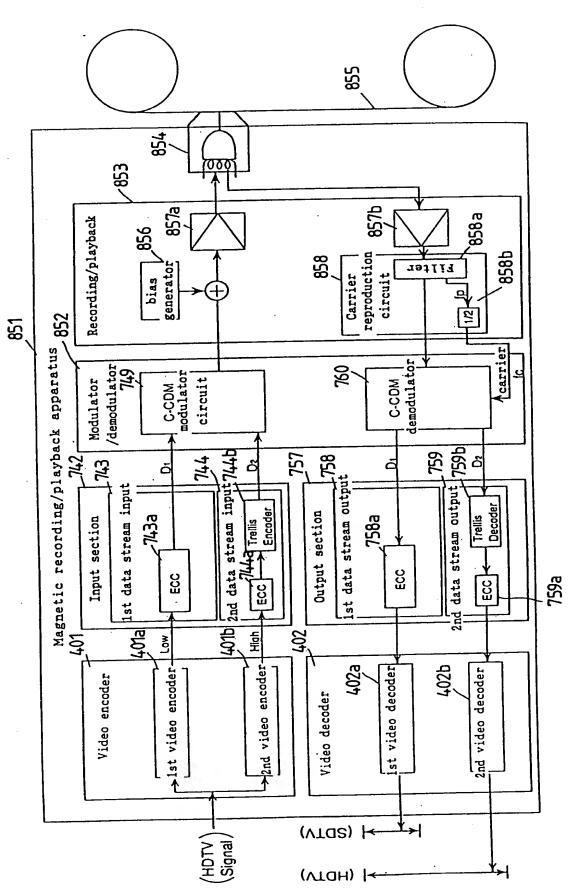
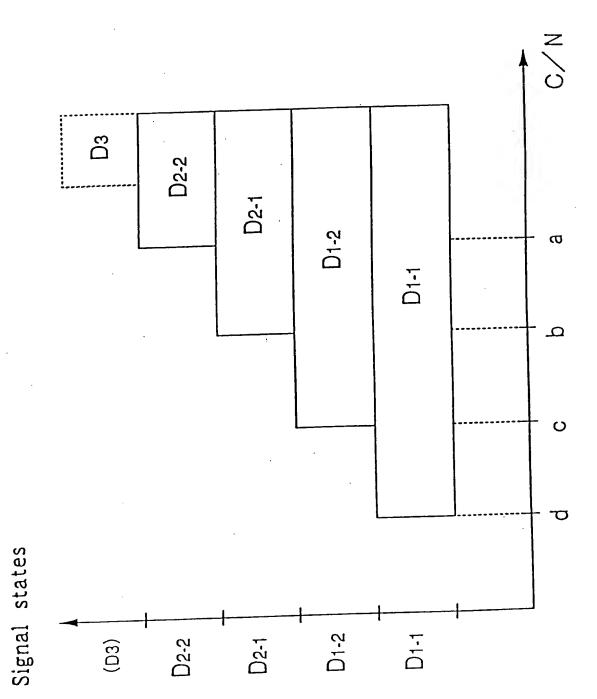


FIG. 85



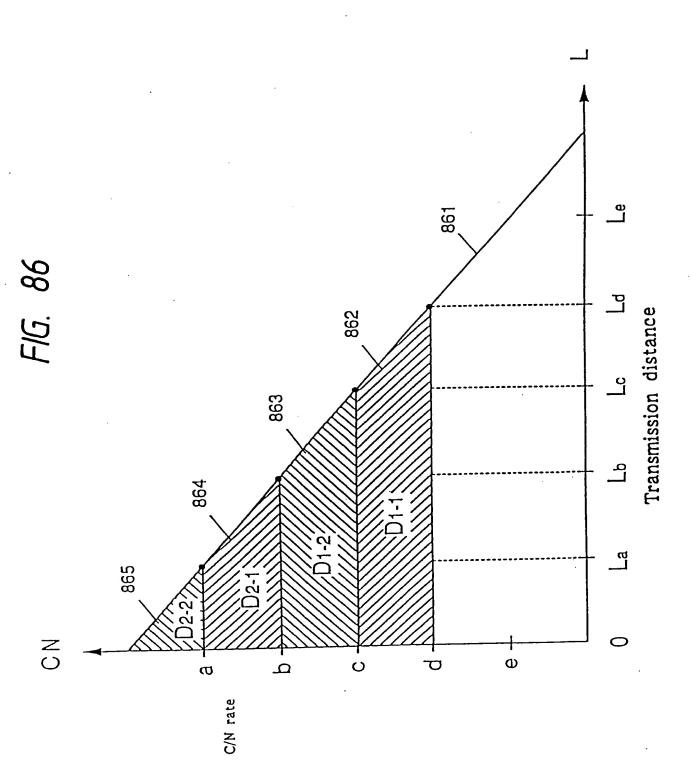


FIG. 87

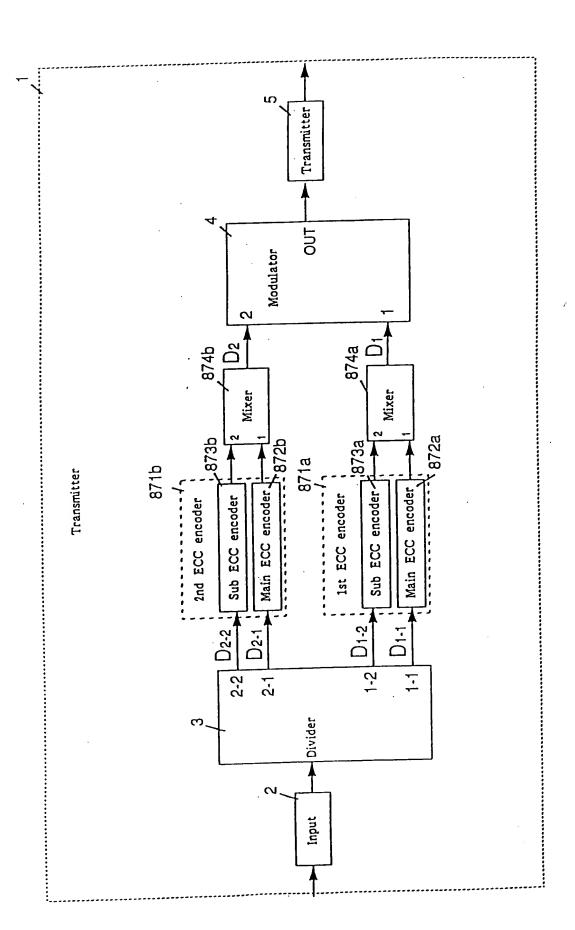
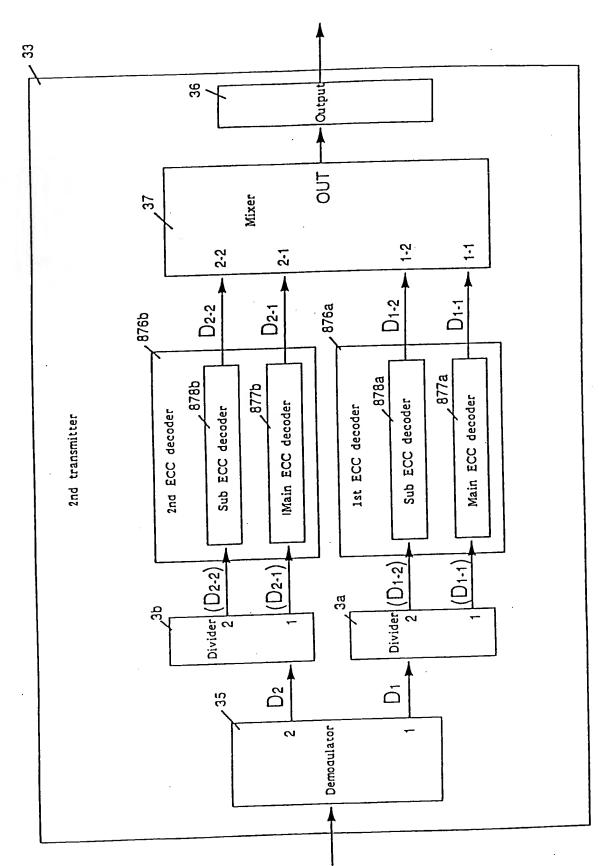
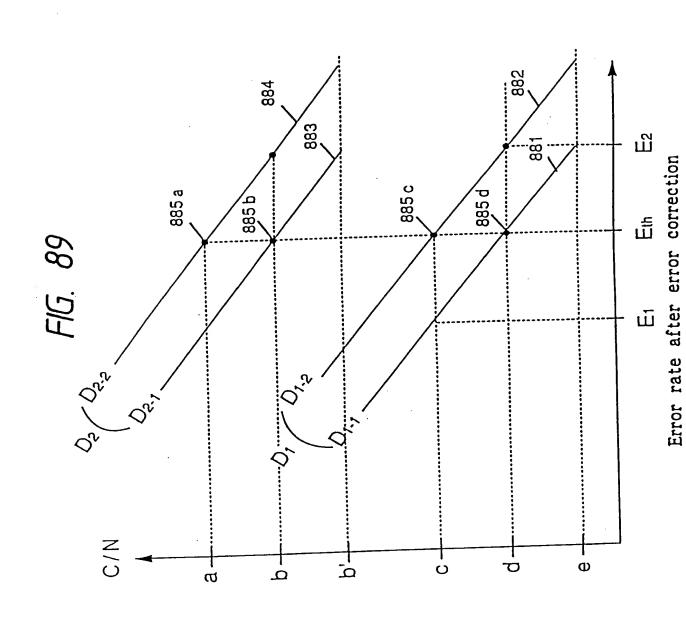
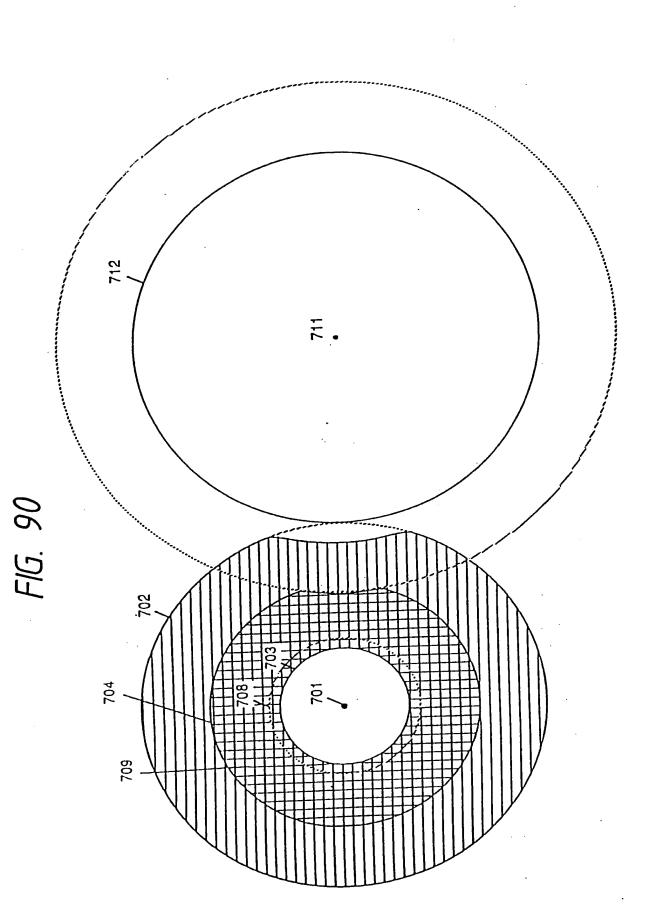
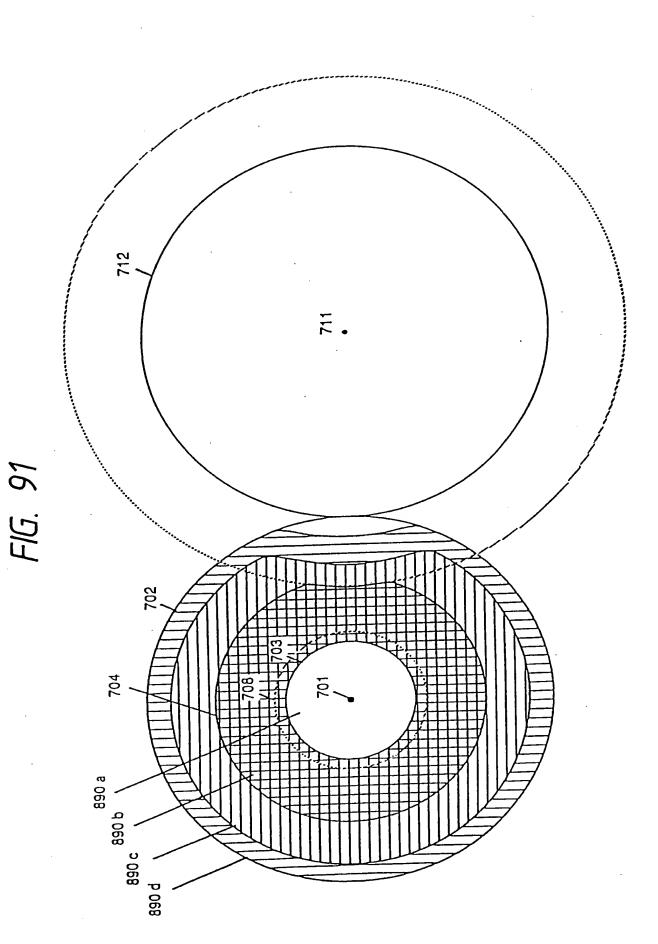


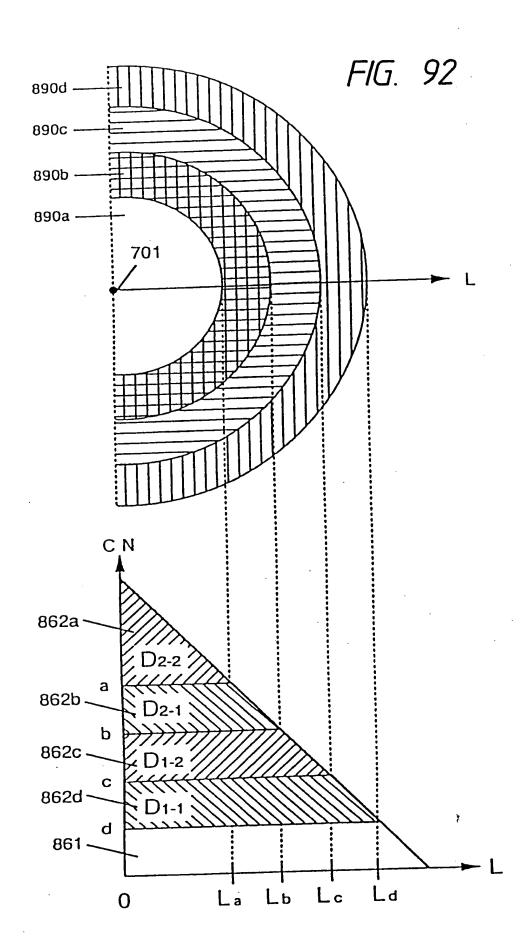
FIG. 88











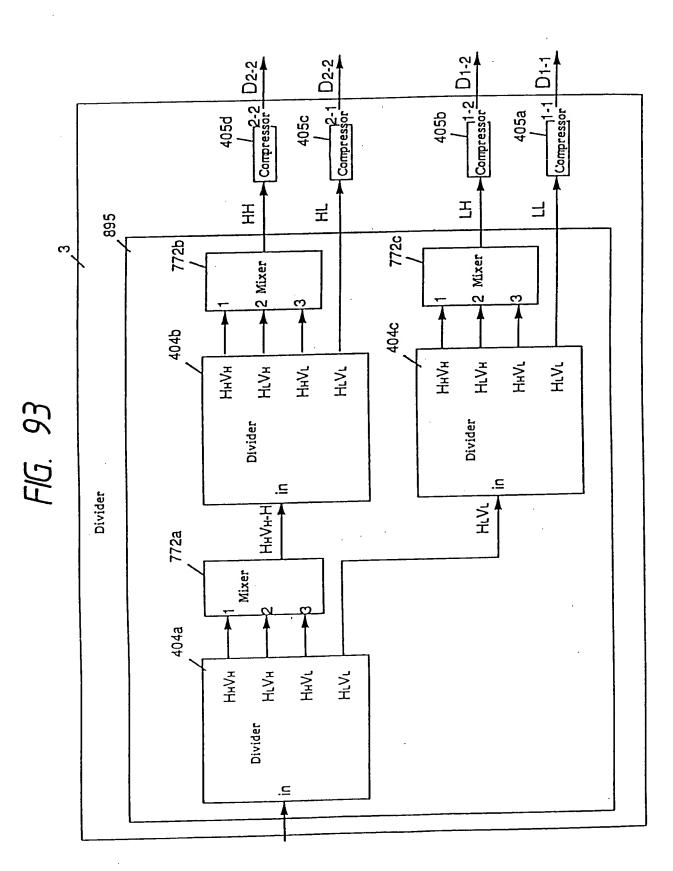
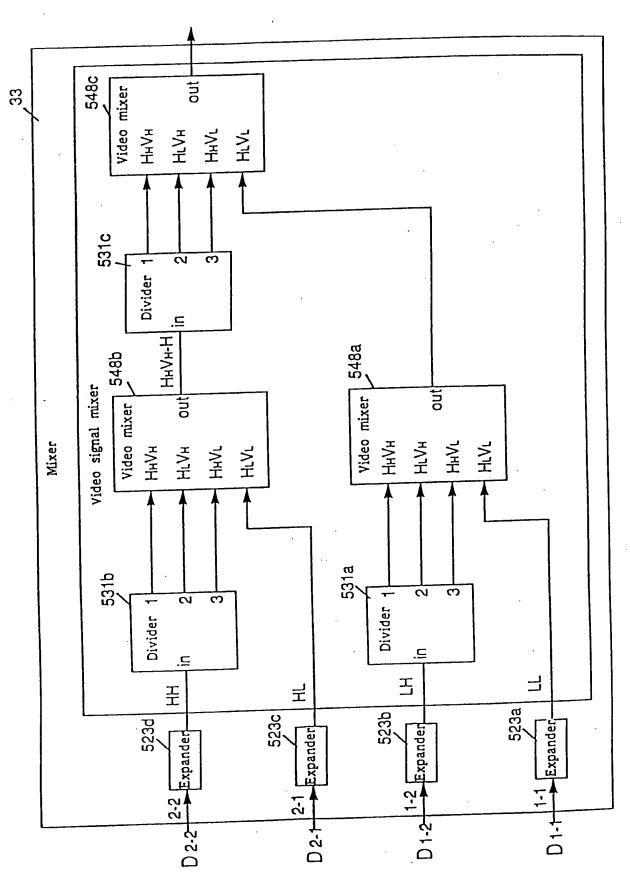
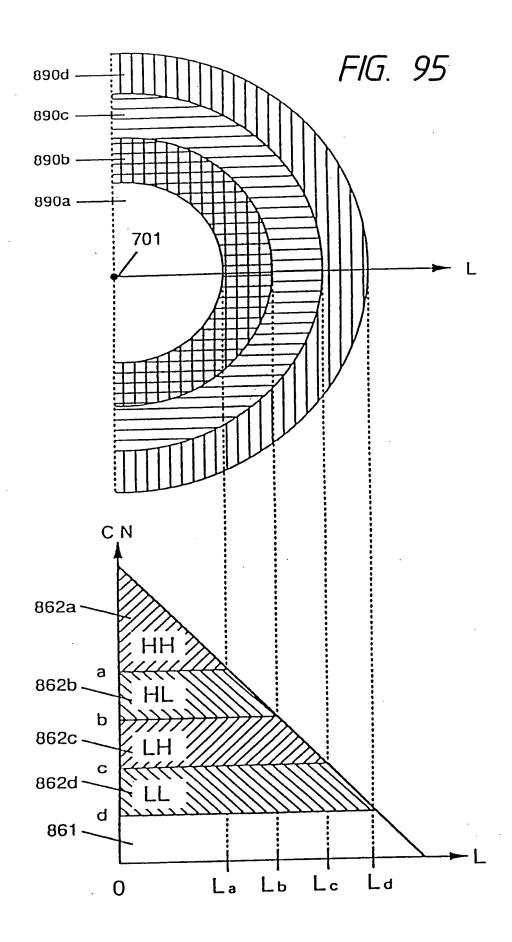
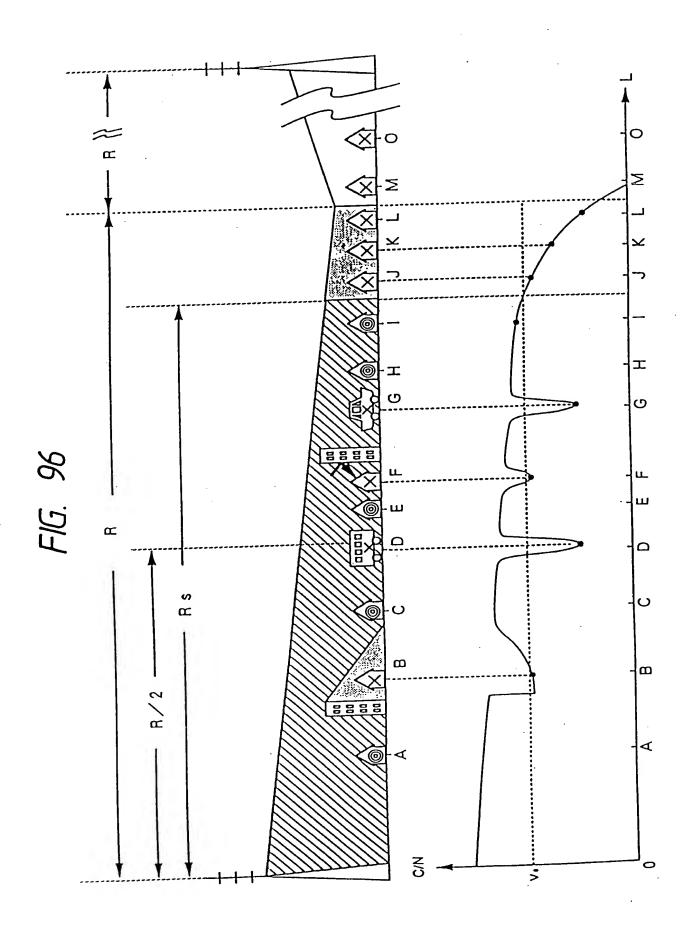
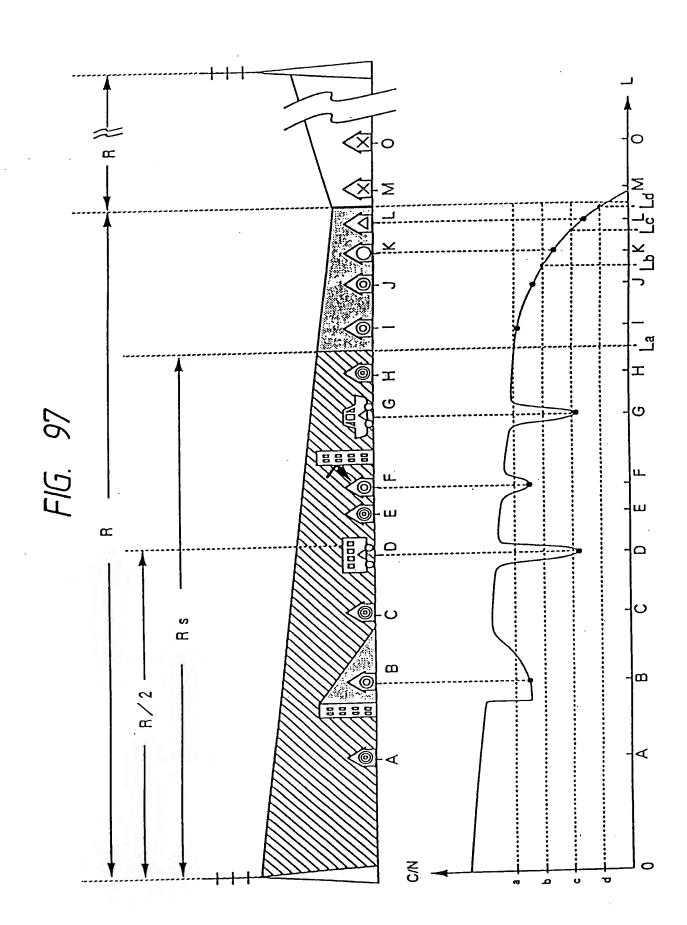


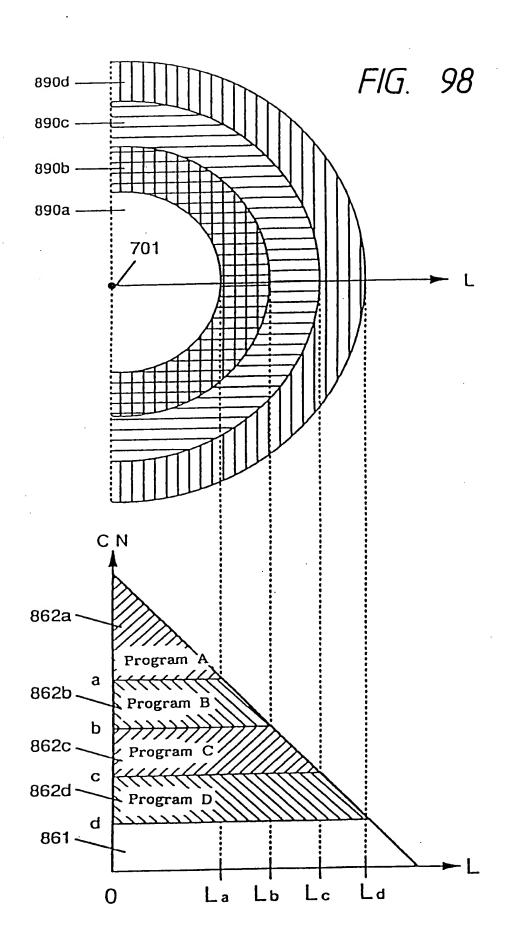
FIG. 94











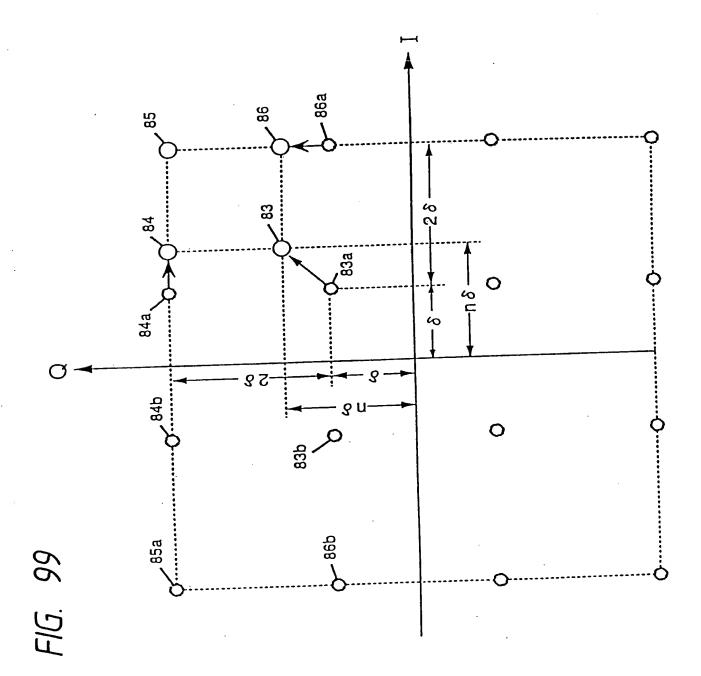


FIG. 100

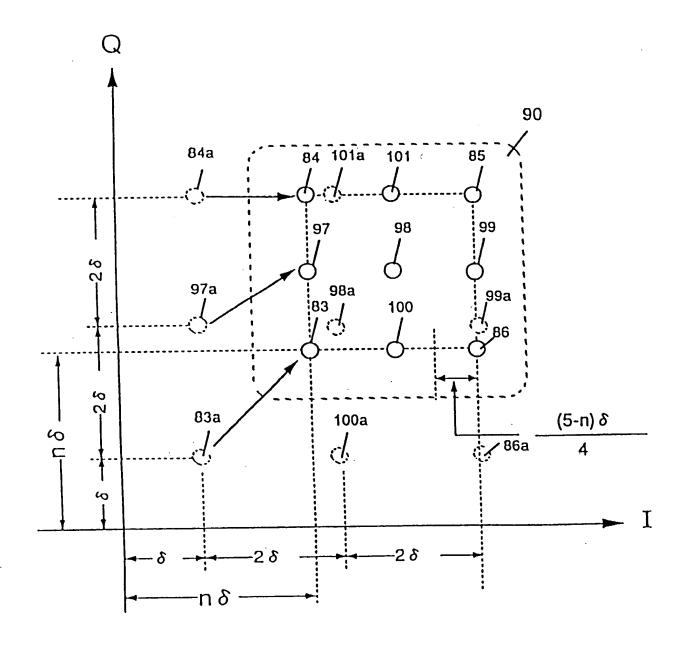


FIG. 101



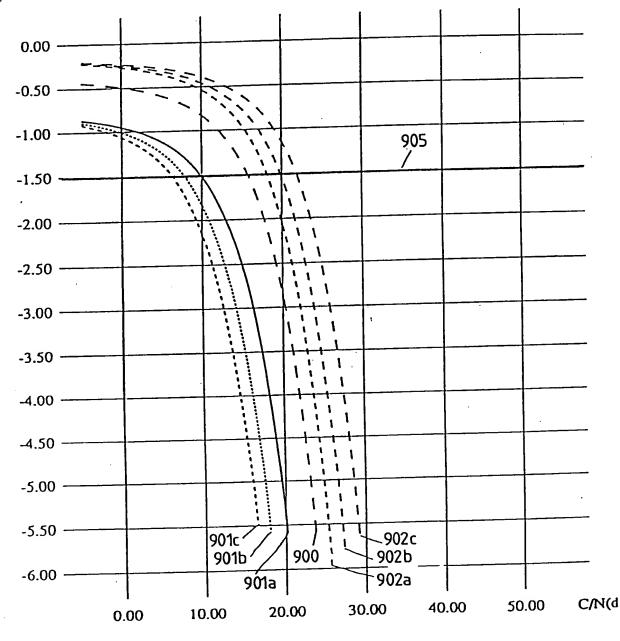


FIG. 102

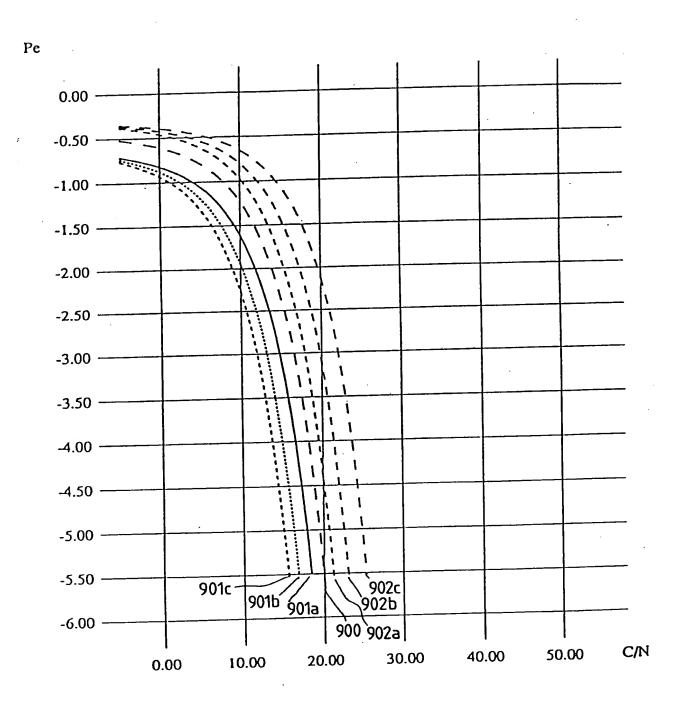


FIG. 103

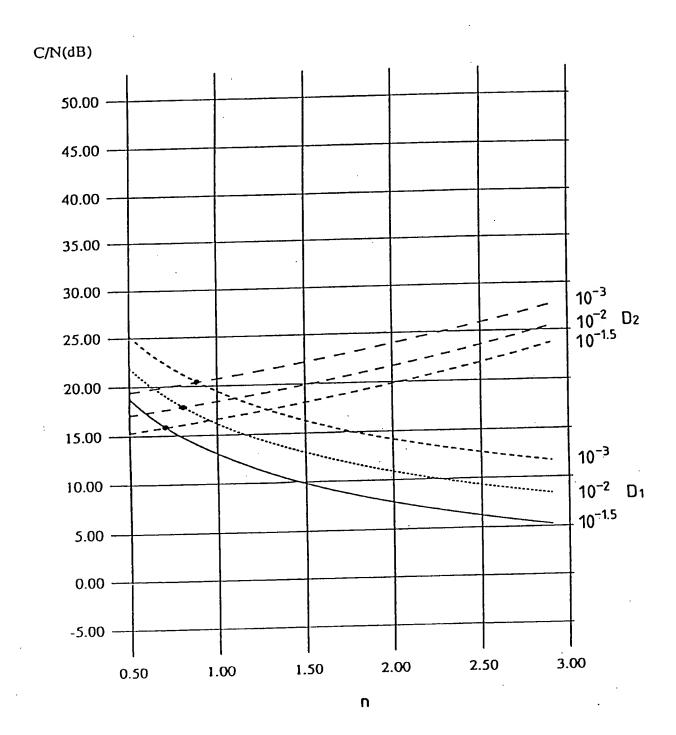
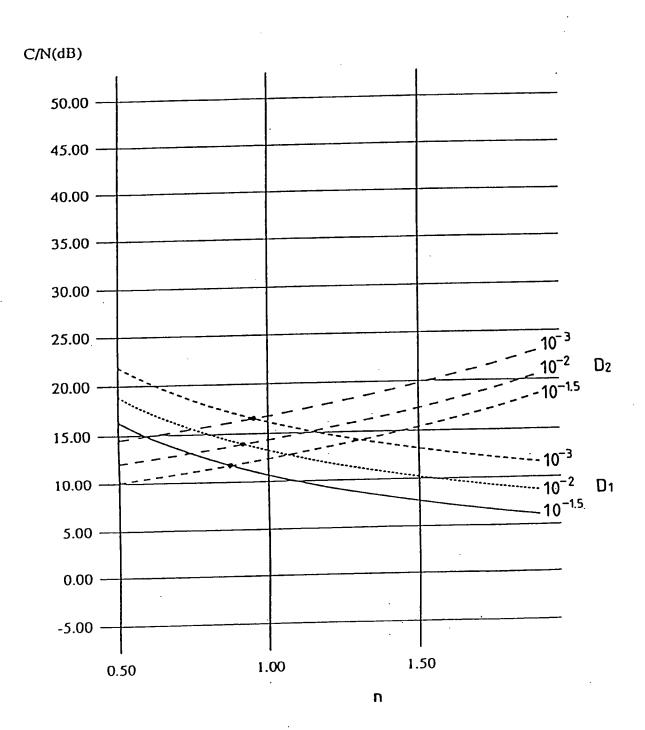


FIG. 104



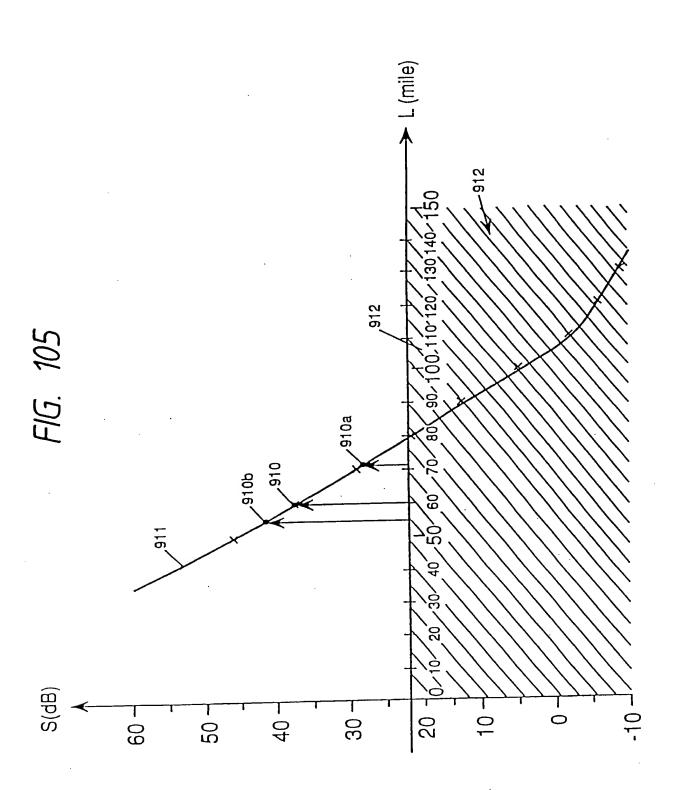
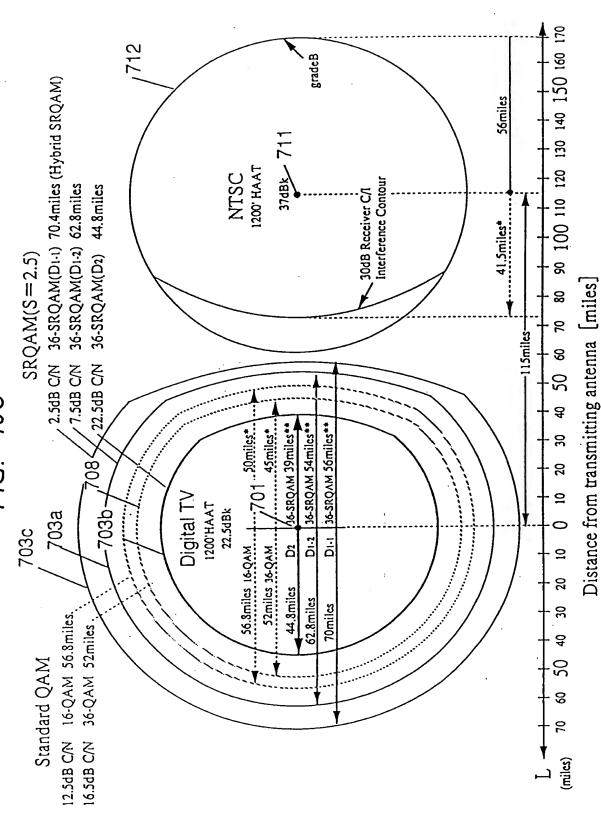
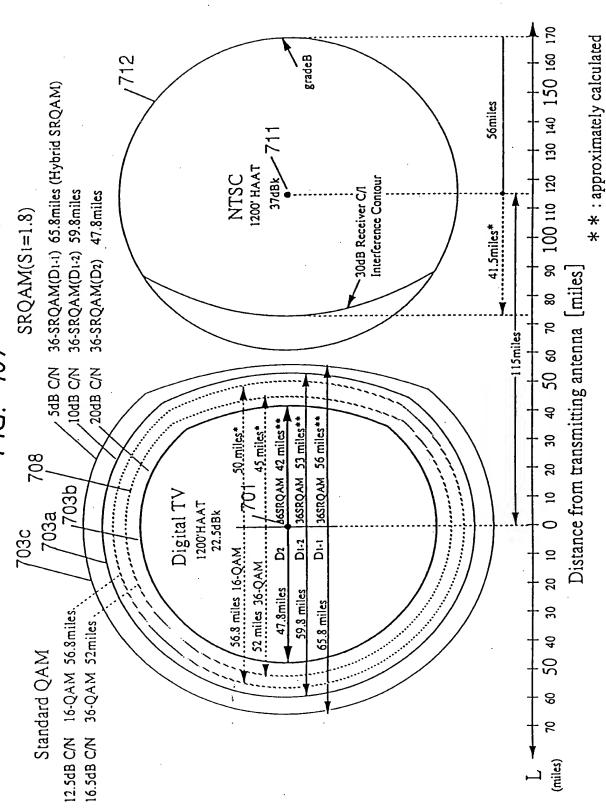


FIG. 106



## FIG. 107



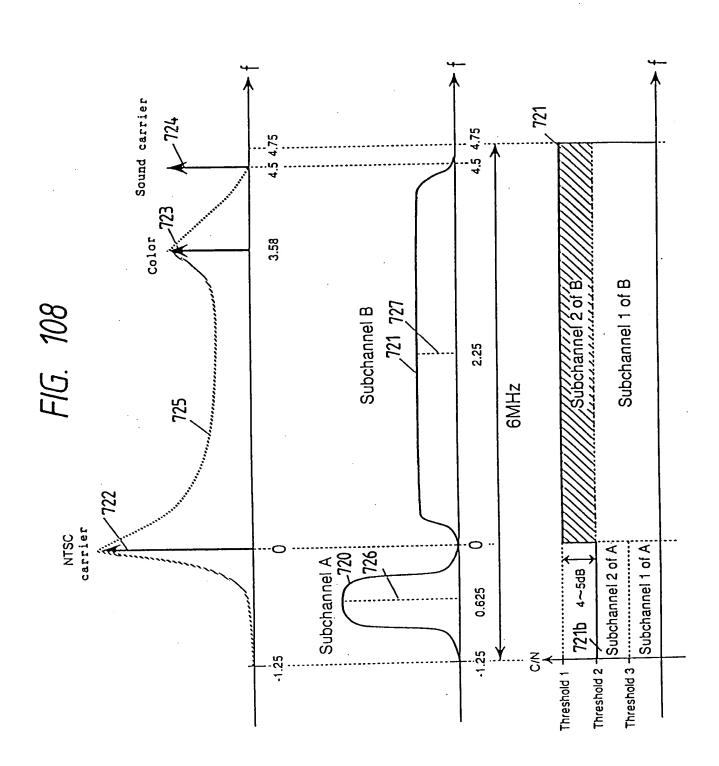
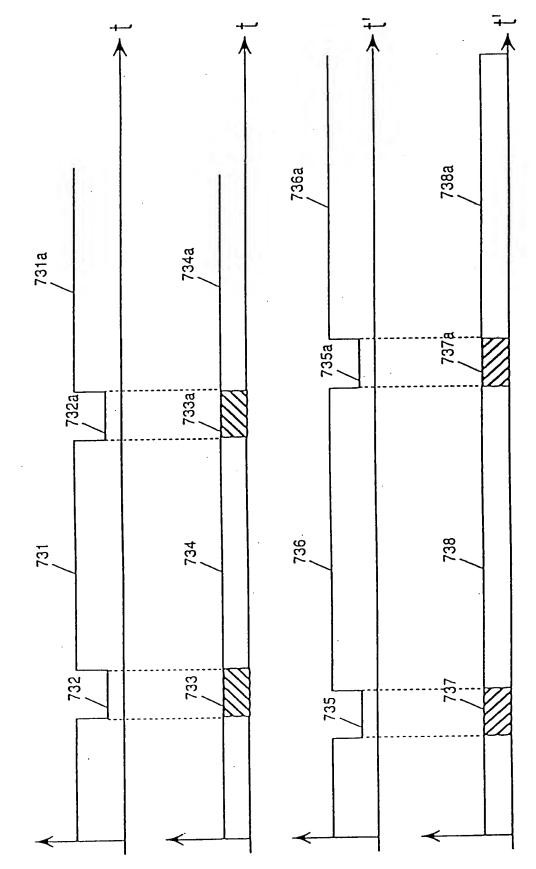


FIG. 109



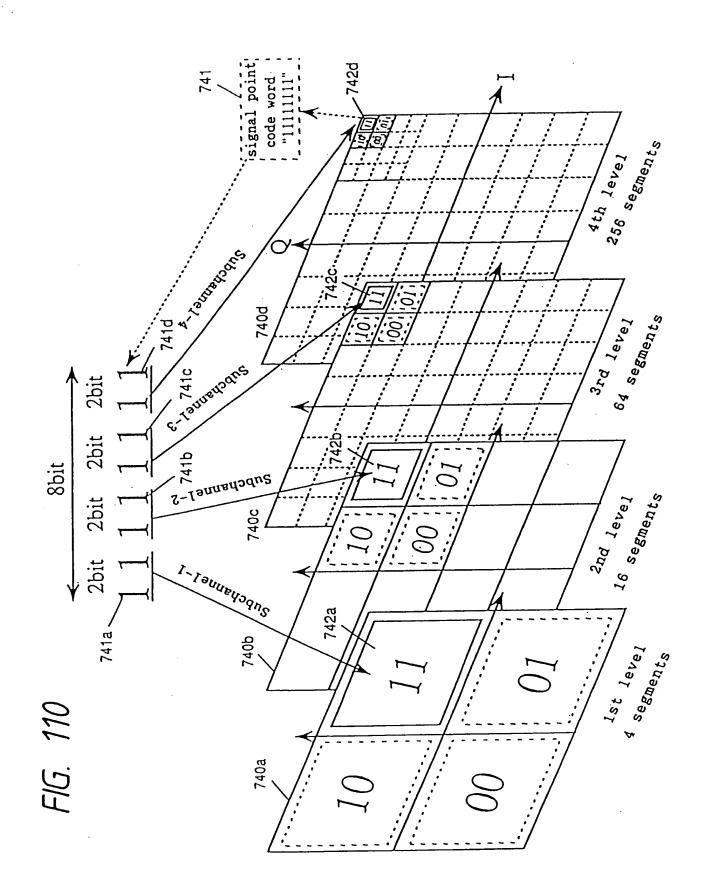
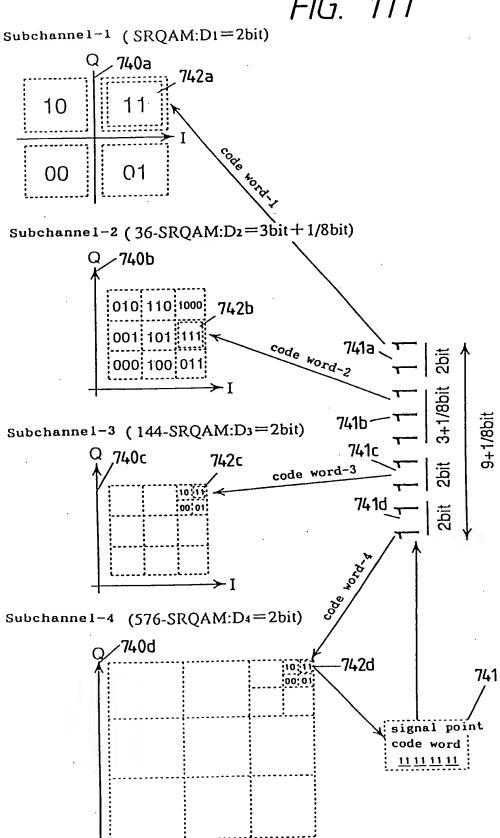
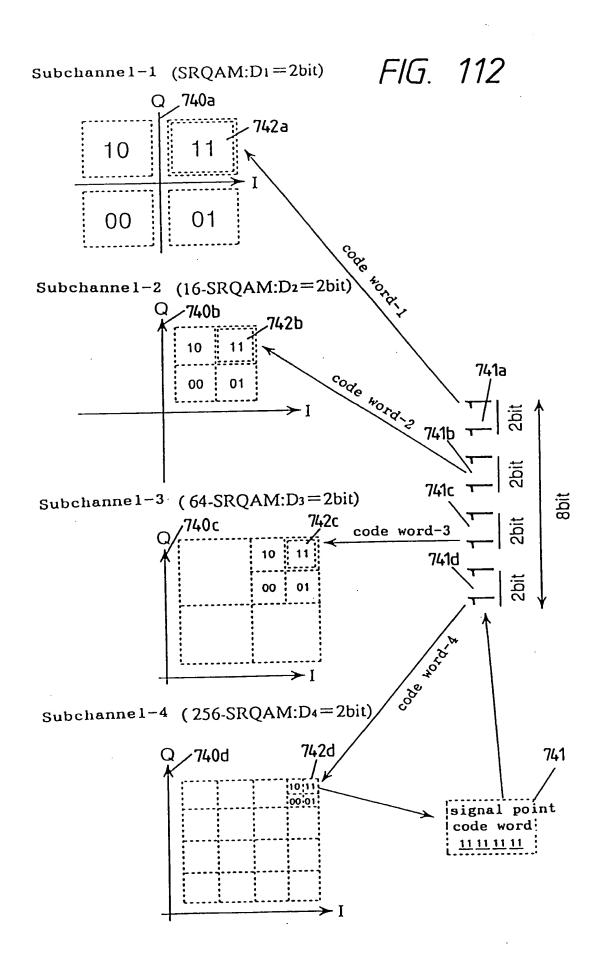


FIG. 111





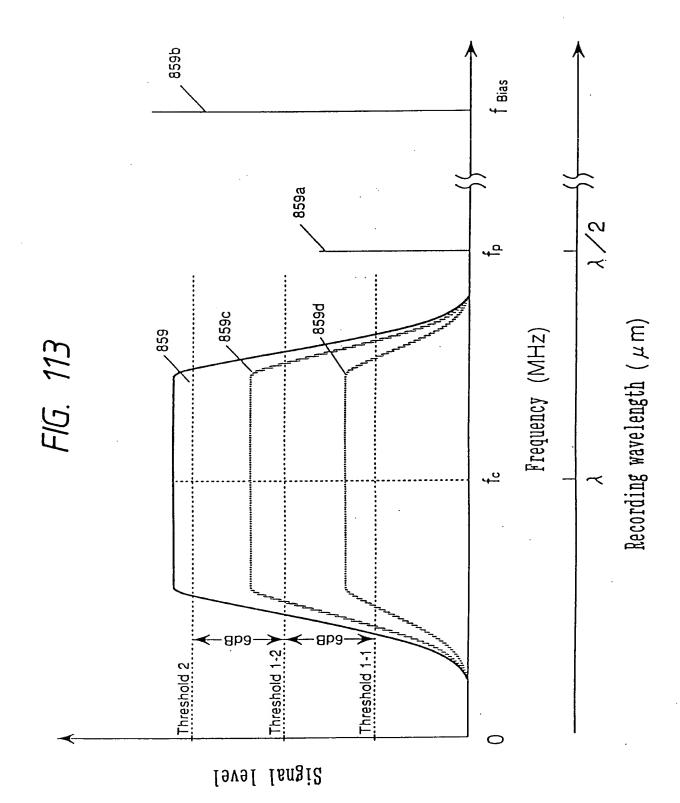


FIG. 114

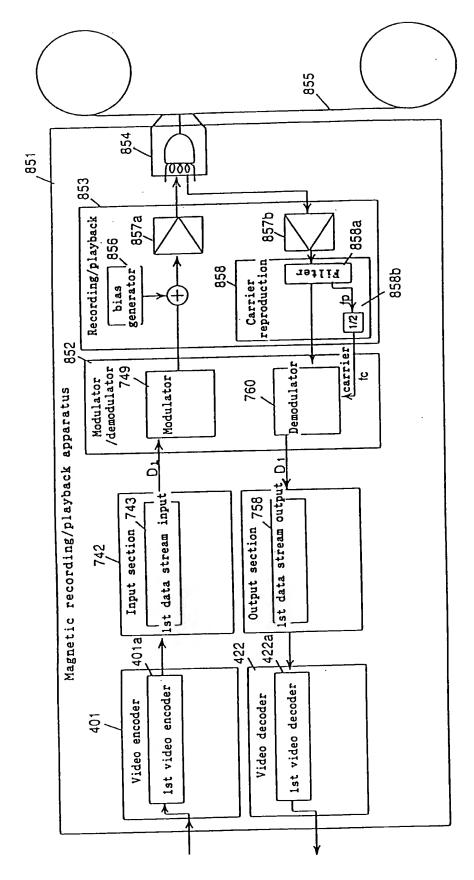
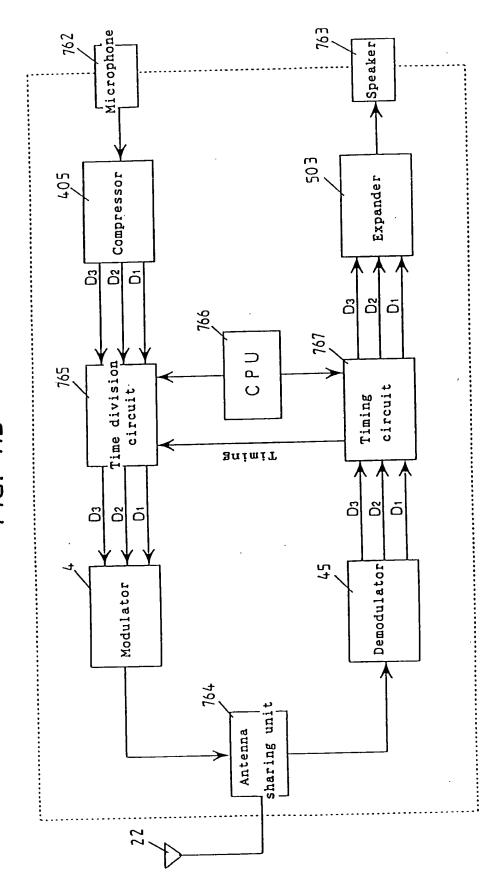
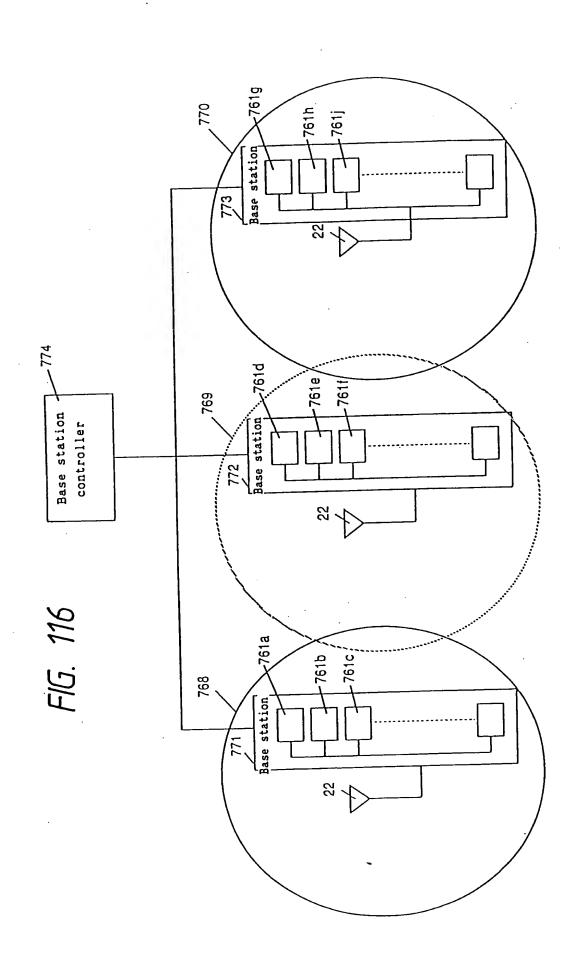
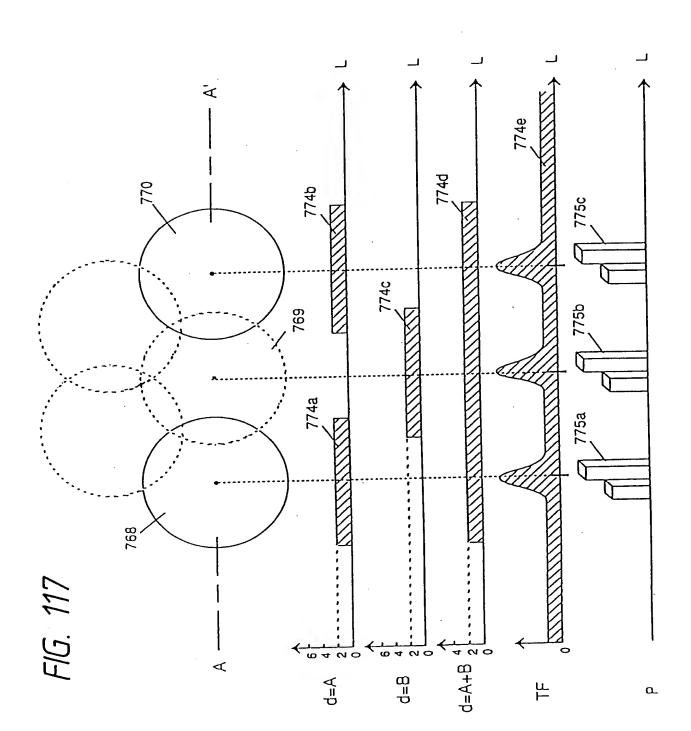
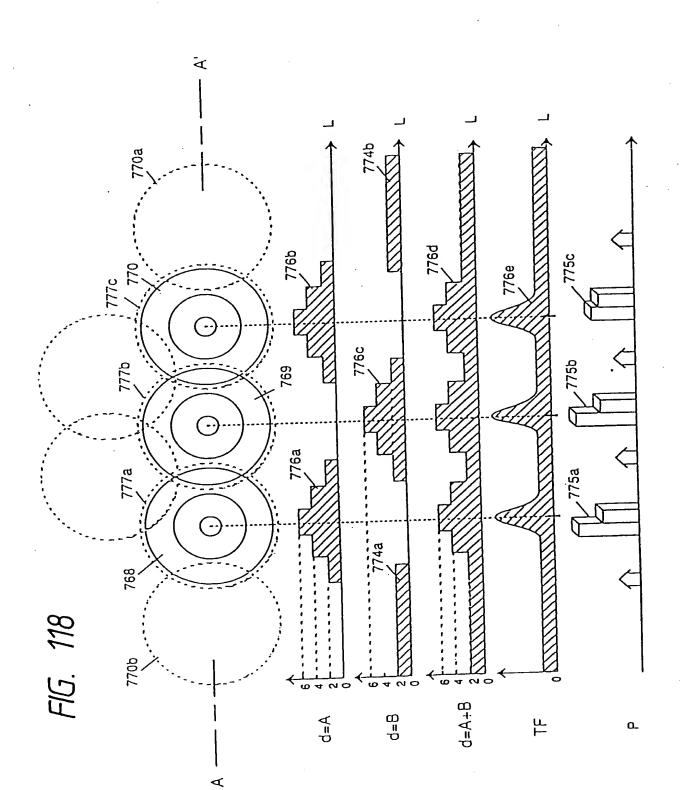


FIG 115









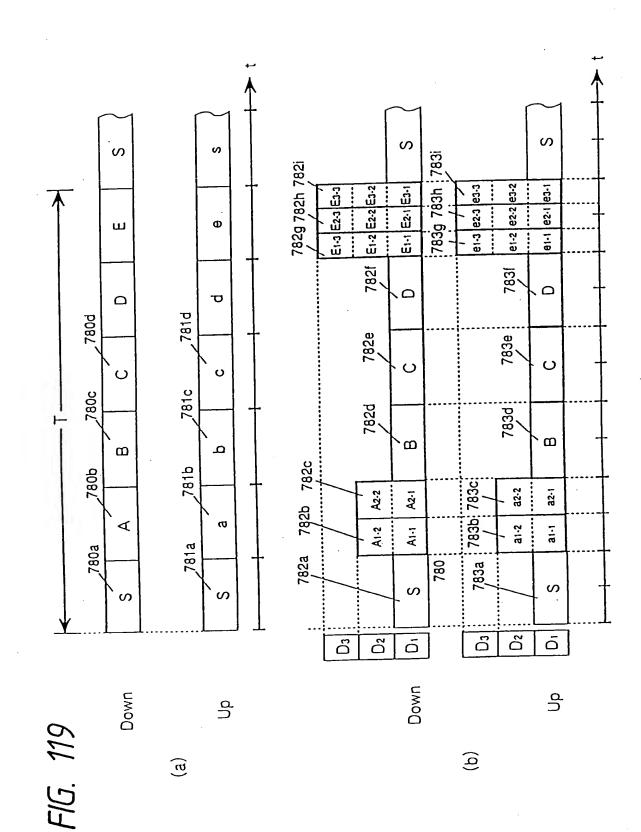
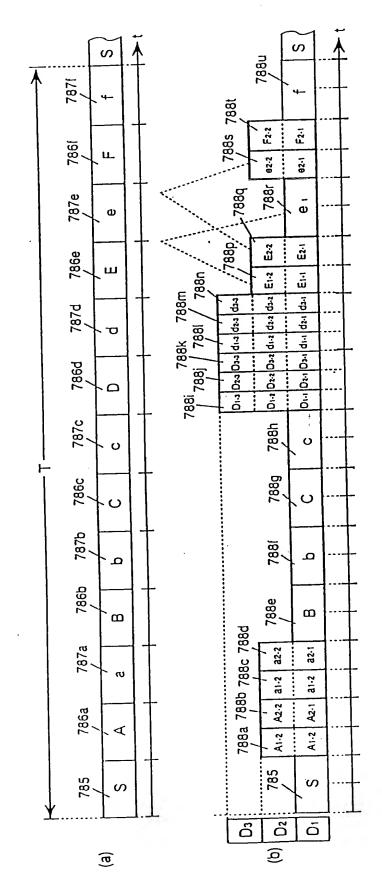
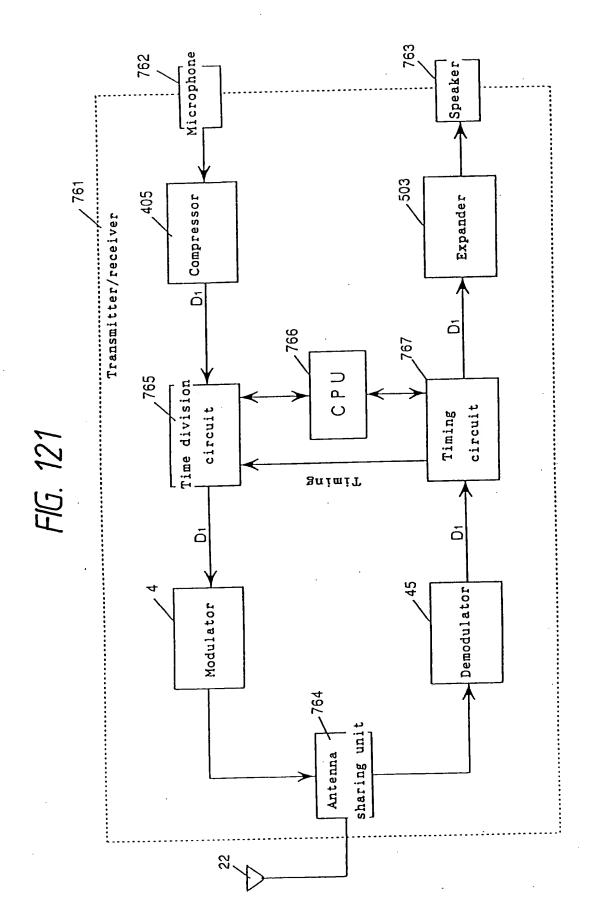


FIG. 120





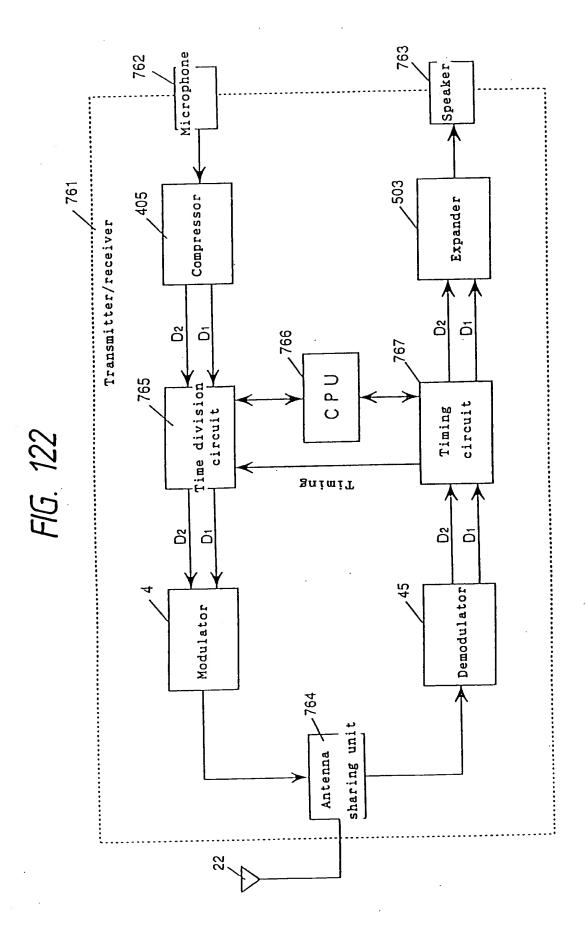
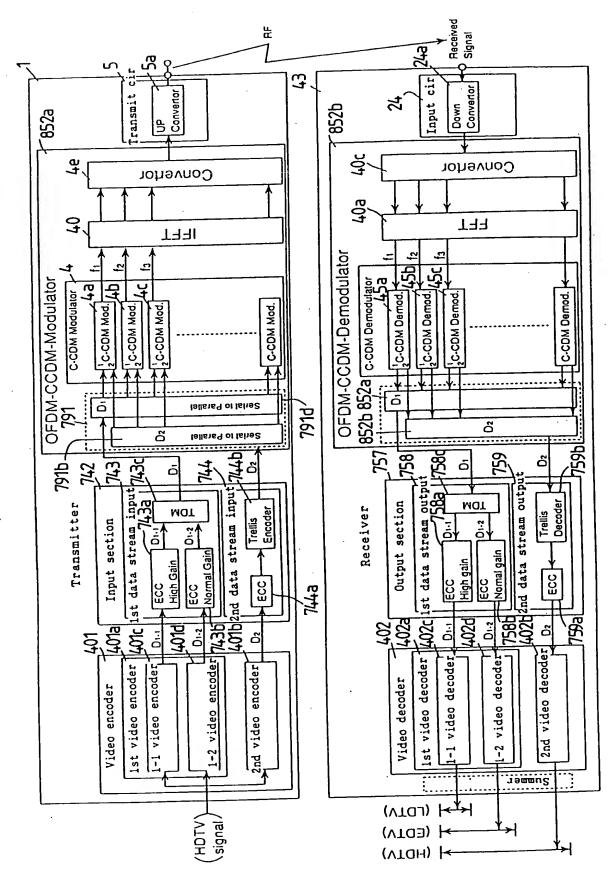
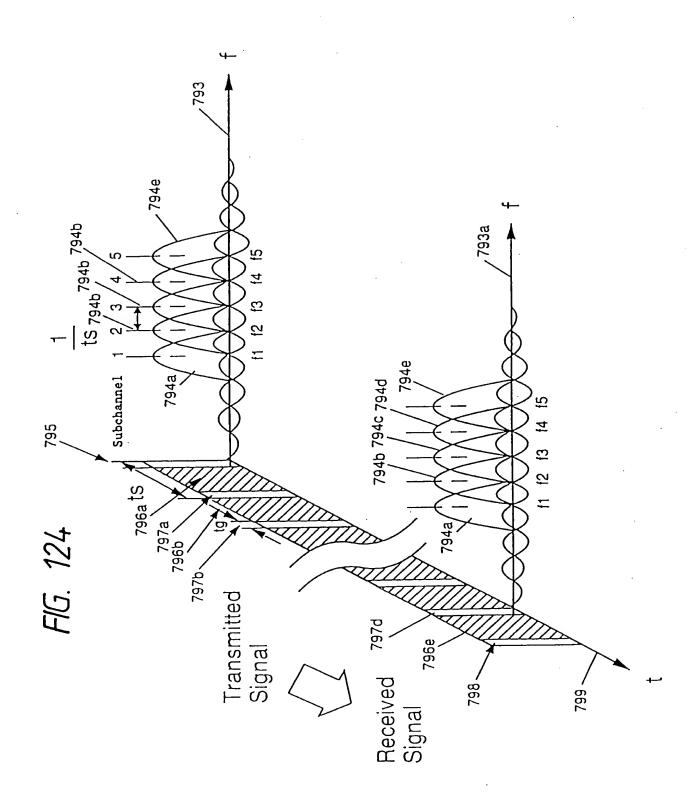


FIG. 123





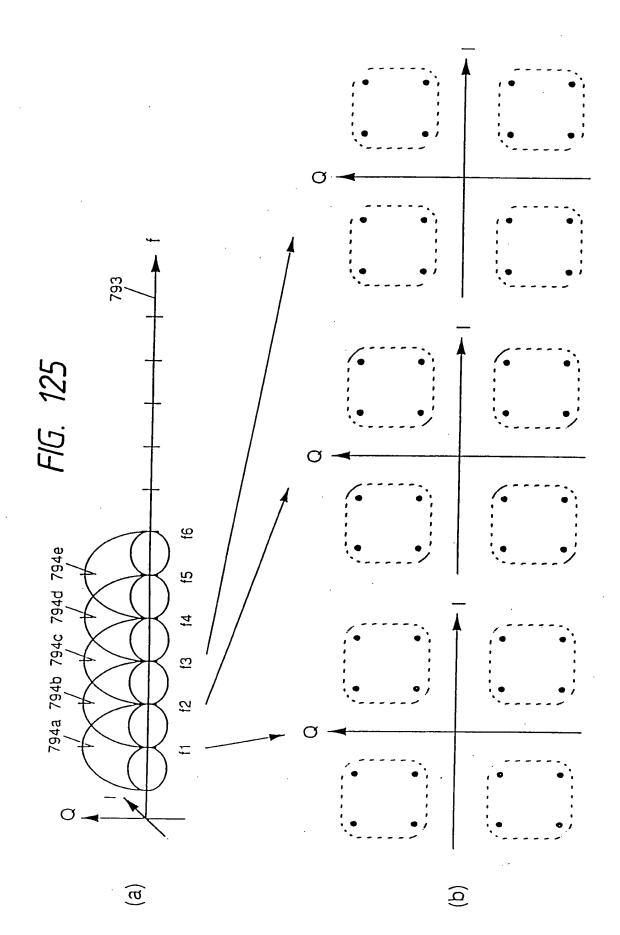
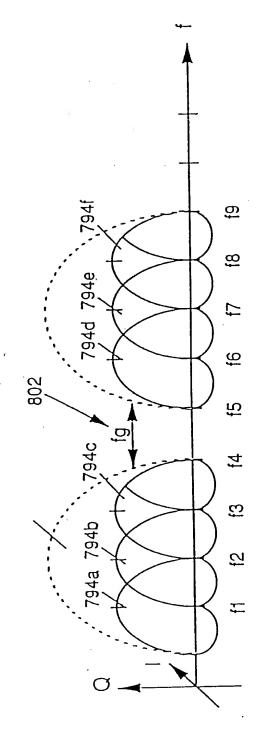


FIG. 126



(a)

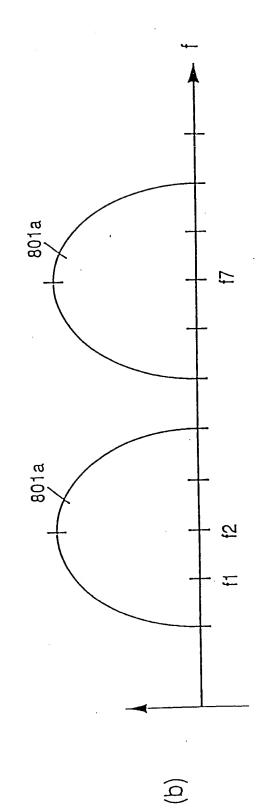
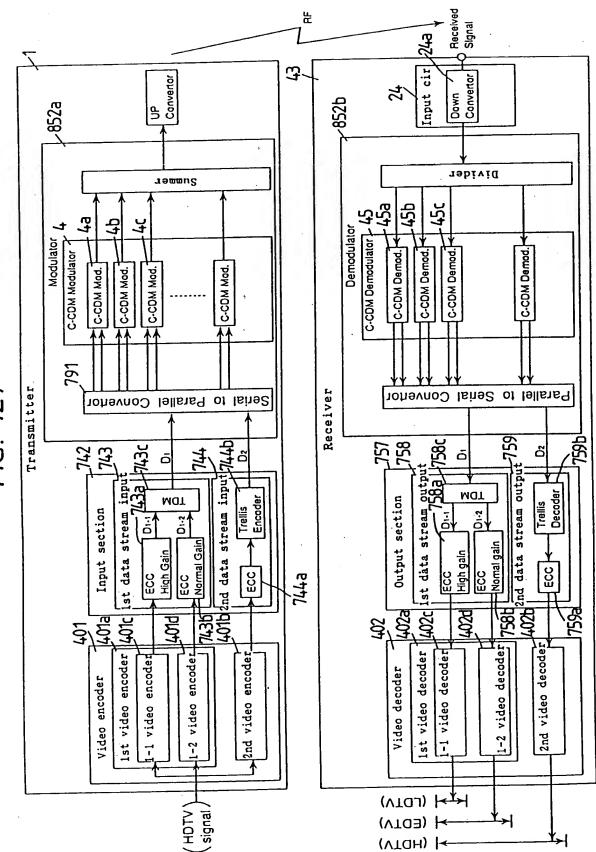
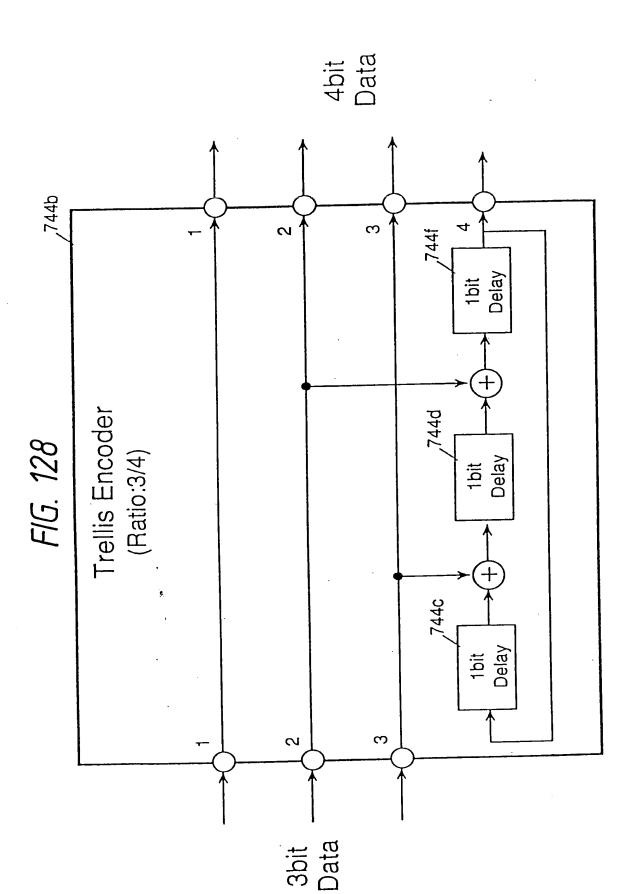


FIG. 127





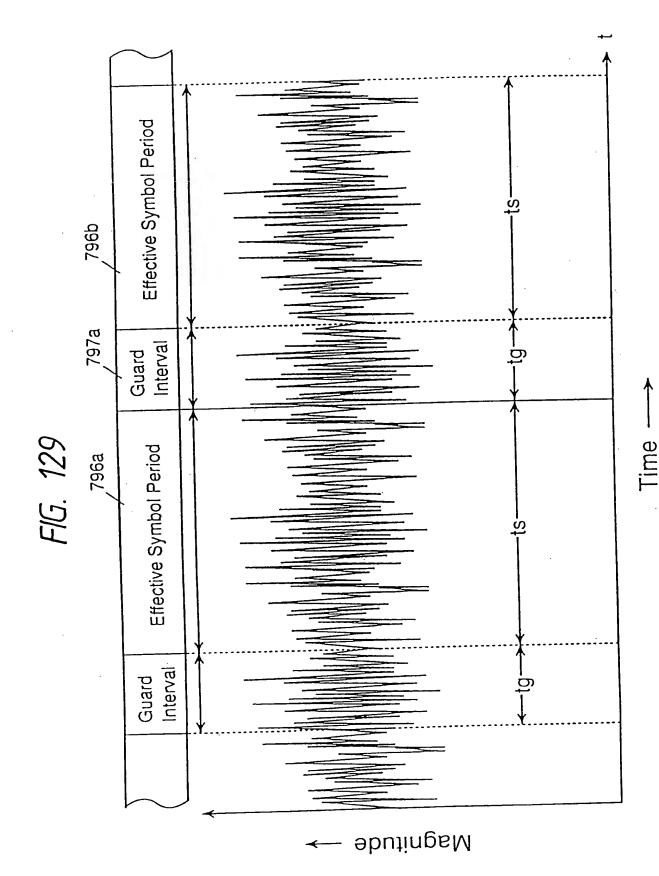


FIG. 130

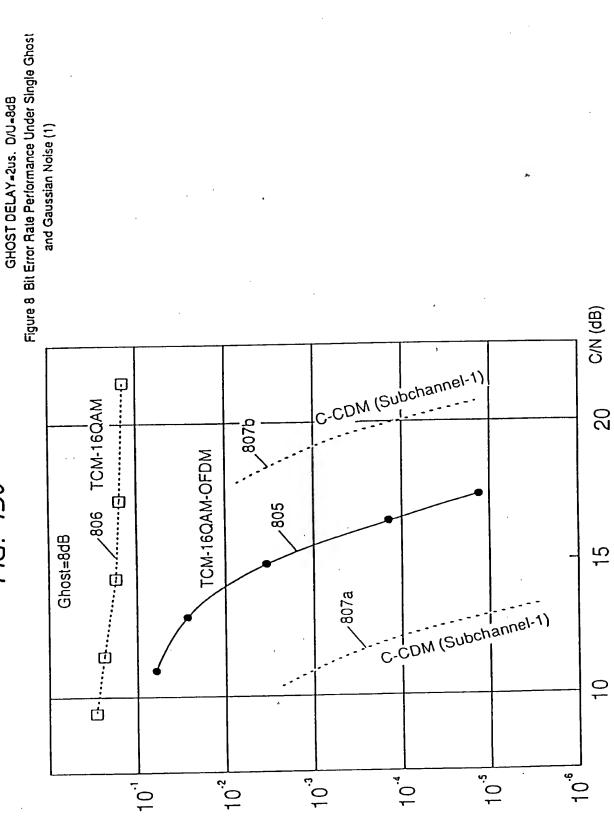
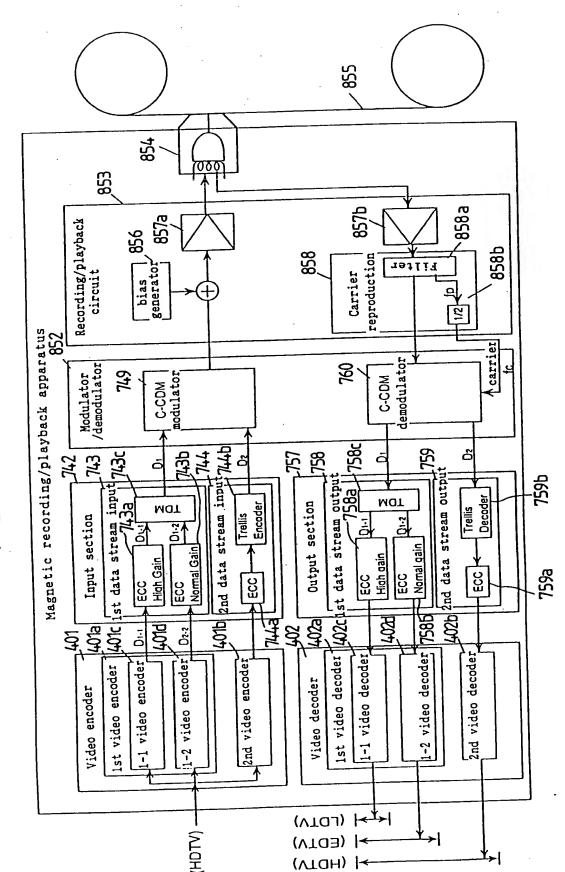
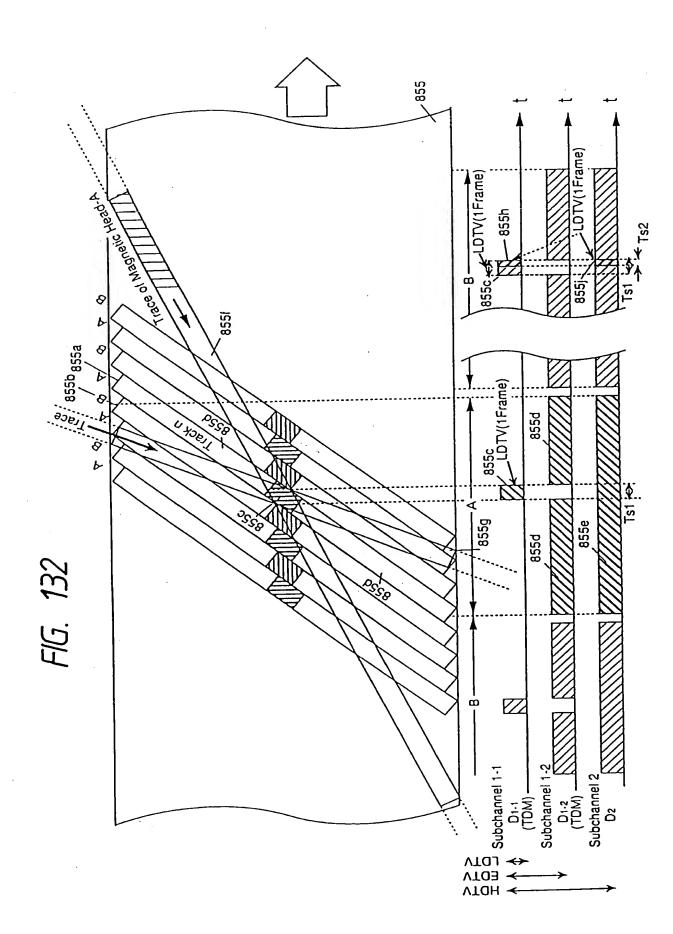
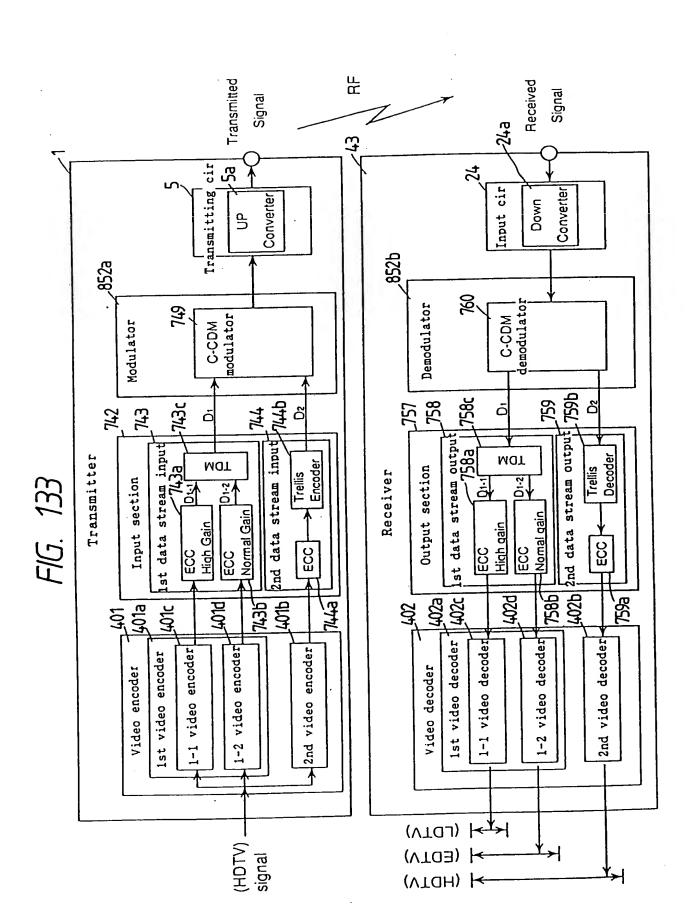


FIG. 131







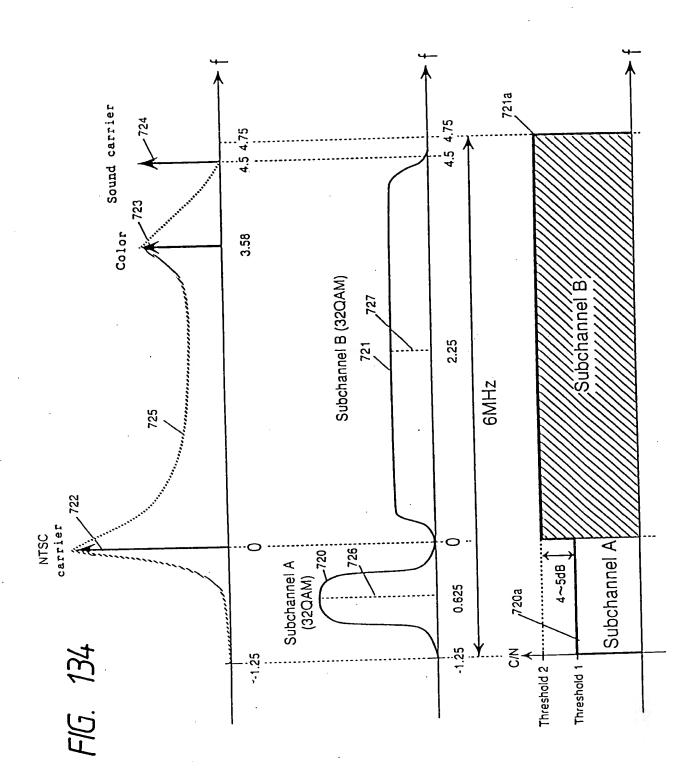
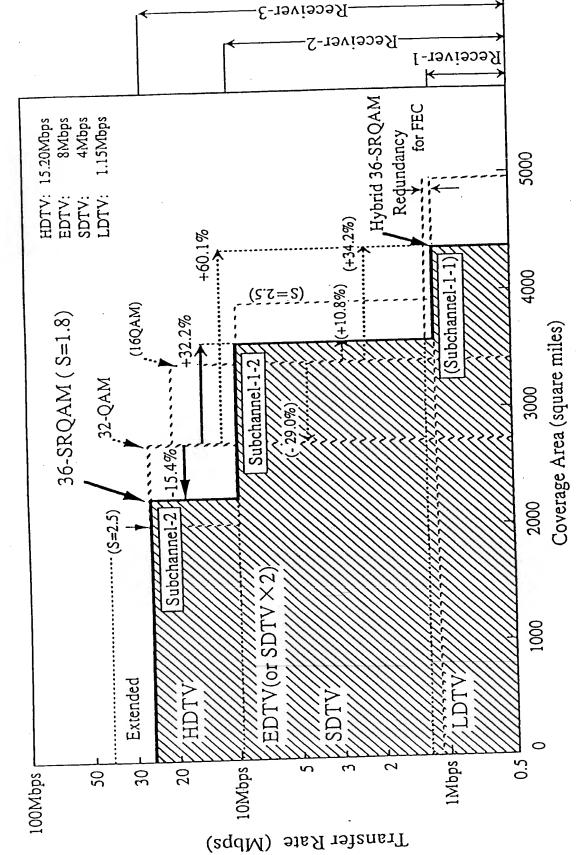
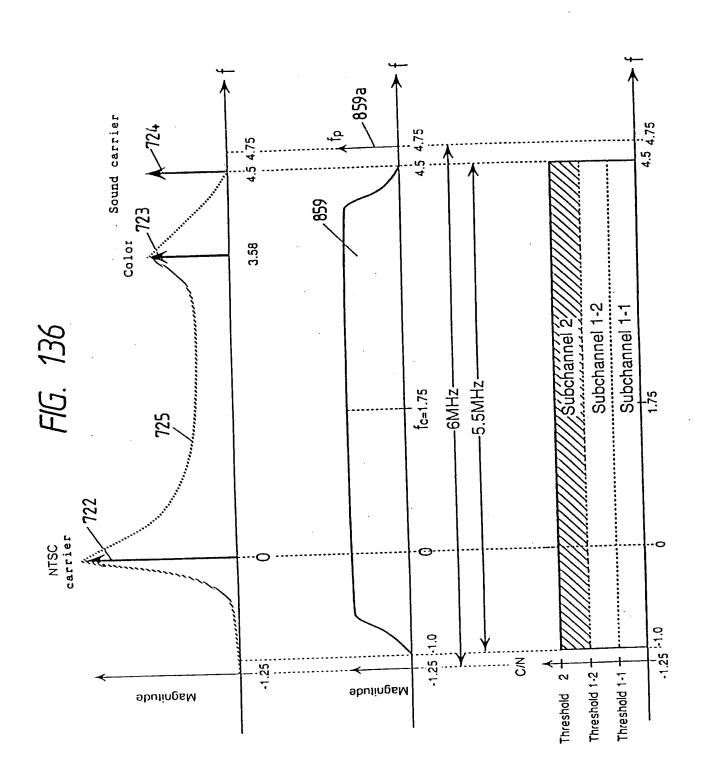


FIG. 135





比 Received **Transmitted** Signal Signal . 24a Ė .5a Transmitting cir Input circui Converter Down Converter g 852b Demodulator 8 C-CDM demodulator 57,0 modulator Modulator C-CDM and data stream output 759b 7585 758 757 739 ŏ 7430 743 747 ō 1st data stream outpui 2nd data stream input Trellis Decoder MOT 1st data stream inpu Receiver Output section MOT 01:2 Transmitter Trellis Input section 01:2 rellis Dec. Nomal gain Trellis Dec. Normal Gain Trellis Enc. Trellis Enc. High gain High Gain ပ္ပ 200 402b 158P 75857 P203 402c 402a 49p 7407 7436 197 401d 401a 5 2nd video decoder 1-2 video decoder 1st video decoder 1-1 video decoder 1-2 video encoderH 1st video encoder 1-1 video encoder 2nd video encoder Video decoder Video encoder (VTaJ) (MDTV) (VTQ3) signal (VTQH) |≺

FIG. 137

